

Compal Confidential

Model Name :  
File Name : LA-8671P

Compal Confidential

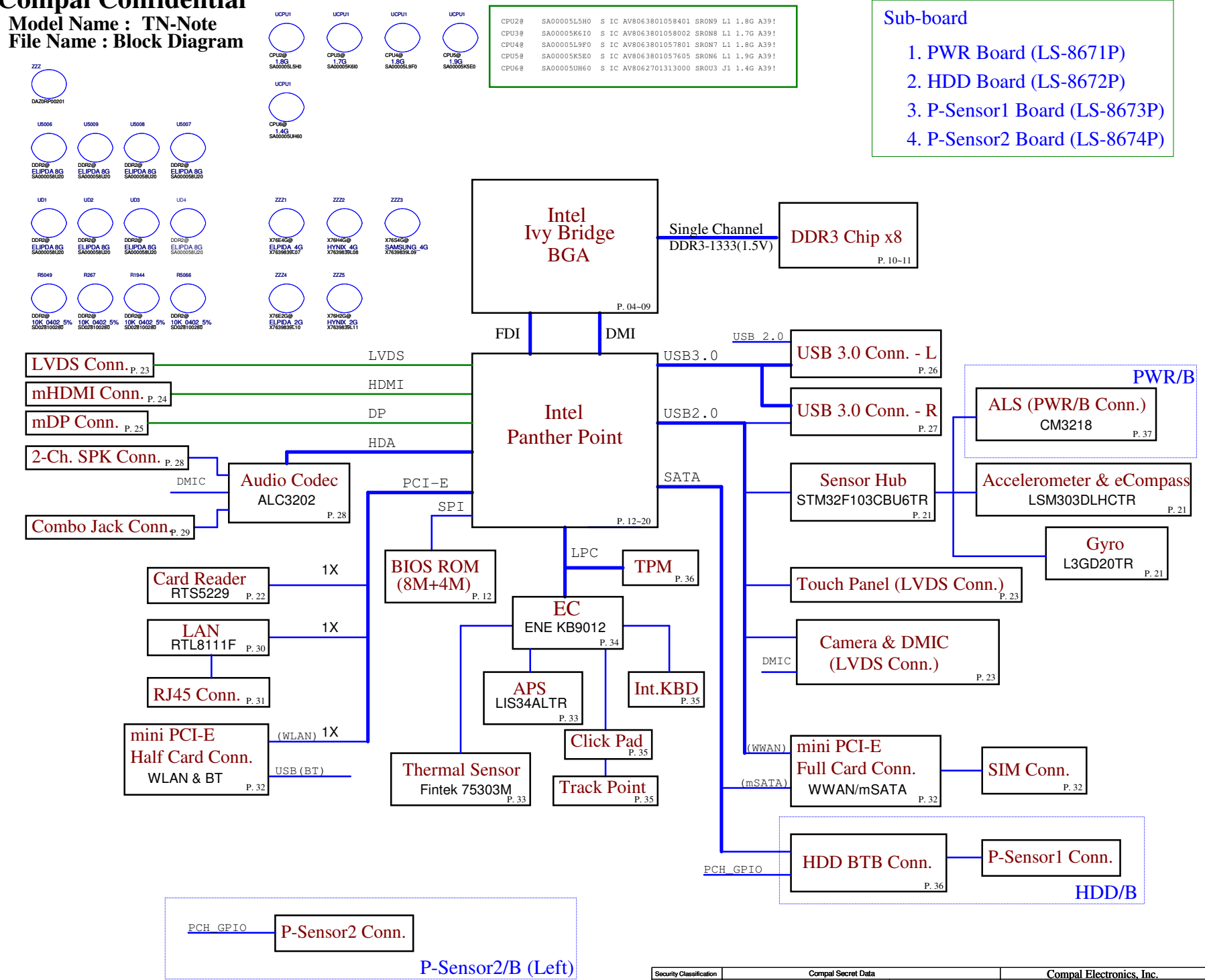
TN-Note Schematic Document

2012-10-04

REV:1A\_1004A

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Compal Confidential  
Model Name : TN-Note  
File Name : Block Diagram



Voltage Rails

Power plane					
State	+B	+5VALW +3VALW	+1.5V	+3VM +1.05VM	+5VS +3VS +1.8VS +1.5VS +1.05VS +VCC_GFXCORE_AXG +CPU_CORE +VCCSA +0.75VS
S0	○	○	○	○	○
S3	○	○	○	○	✗
M3	○	○	○	○	✗
S4/S5 - AC	○	○	✗	✗	✗
S4/S5 - BATT ONLY	○	✗	✗	✗	✗
S4/S5 - NO AC & BATT	✗	✗	✗	✗	✗

BOM Structure

	SDV	FVT	SIT	SVT	SOVP
@ : No Stuff					
ME@ : ME components					
CPUx@ : CPU SKU	V	V	V	V	V
DDRx@ : RAM SKU	V	V	V	V	V
SBA@ : SBA	V	V	V	V	V
NOSBA@ : NO SBA					
ID4@ : Intel Deep S4	V				
AOAC@ : AOAC	V	V	V	V	V
TPM@ : TPM		V	V	V	V
Short@ : 0ohm short pad					

STATE \ SIGNAL	SLP_S3#	SLP_S4#	SLP_S5#	+VALW	+V	+VS
Full ON	H	H	H	ON	ON	ON
S3 (Suspend to RAM)	L	H	H	ON	ON	OFF
S4 (Suspend to Disk)	L	L	H	ON	OFF	OFF
S5 (Soft OFF)	L	L	L	ON	OFF	OFF

BOARD ID Table

Board ID	PCB Revision
0	0.1
1	0.2
2	0.3
3	0.4
4	0.5
5	
6	
7	

EC SM Bus1 address

Device	Address	HEX	Device	Address	HEX
Smart Battery	0001-011xb	16H	Thermal sensor	1001-101xb	9AH
Charger	0001-0010b	12H	PCH (SML1DATA / GPIO75)	1001-0110 b	96H

EC SM Bus2 address

SM Bus Controller

Device	Address	HEX
Security Rom	1010-100xb	A8 H
DDR DIMMO	1010-000xb	A0 H

SM Bus Controller 0

Device	Address	HEX
No-use	No-use	No-use

Sensor HUB

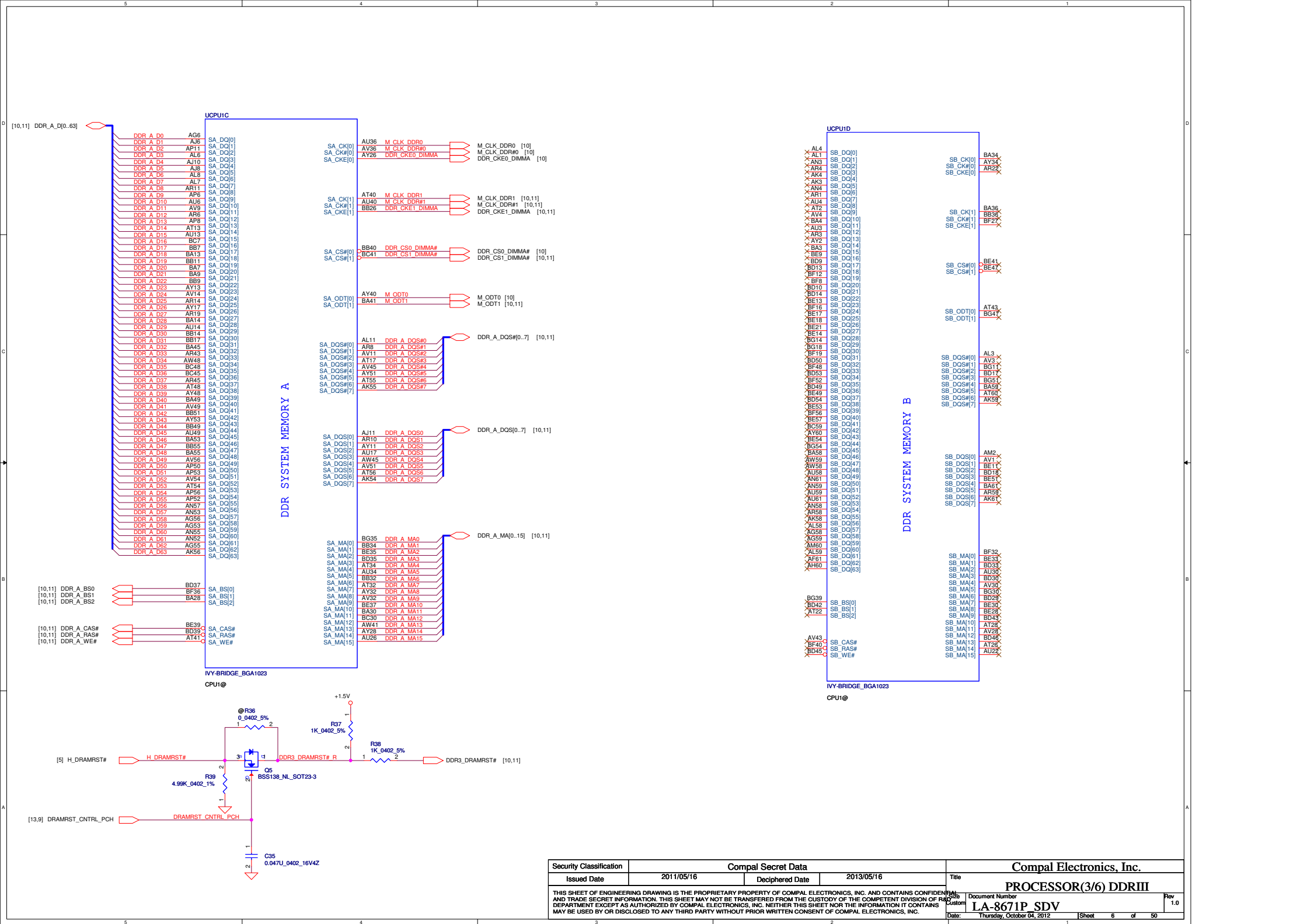
Device	Address	HEX
ALS	0100-100xb	48h
APS	0011-001xb	32h
Gyroscope	0110-101xb	6Ah
e-Compass	0011-110xb	3Ch

USB3.0	USB2.0	NOTE
1	0	
2	1 *	USB3.0/2.0 Conn
3	2	USB3.0/2.0 Conn
4	3	
	4	Sensor Hub
	5	USB Camera
	6	✗
	7	✗
	8	Touch Panel
	9 *	WWAN
	10	WLAN
	11	Finger Printer**
	12	
	13	Bluetooth**

\* Debug Port  
\*\* Not Use

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								Document Number		1.0	
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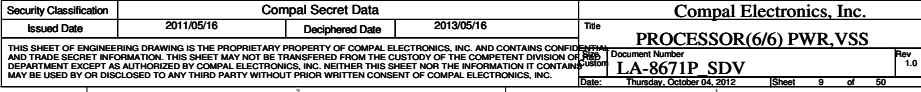




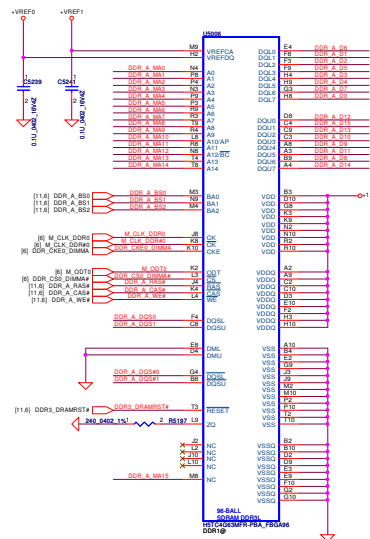




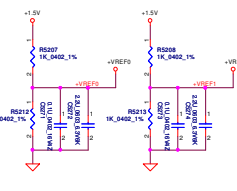
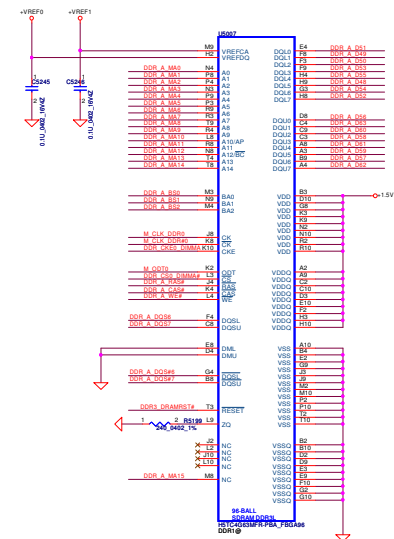
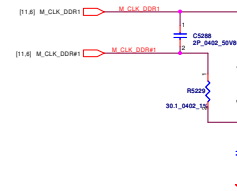
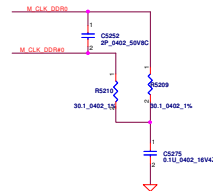
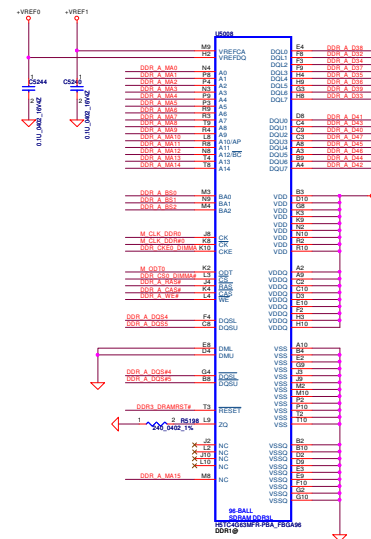
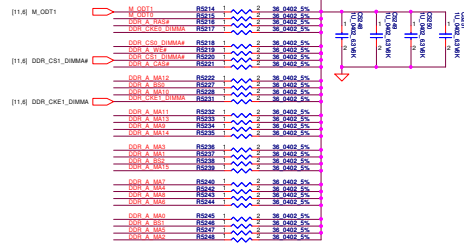
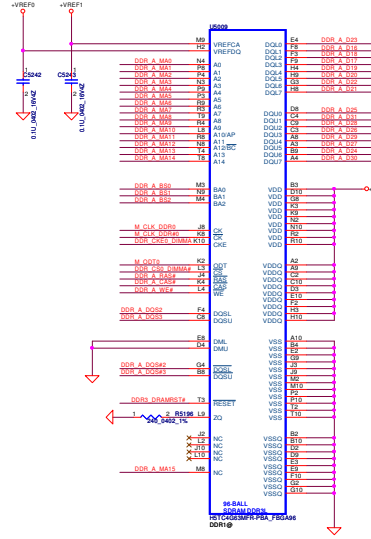
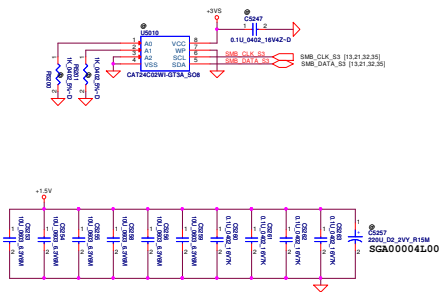




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 [114] DDR\_A\_D054P-7  
 [114] DDR\_A\_D054N-7  
 [114] DDR\_A\_D054P-7

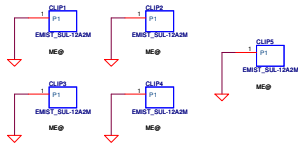
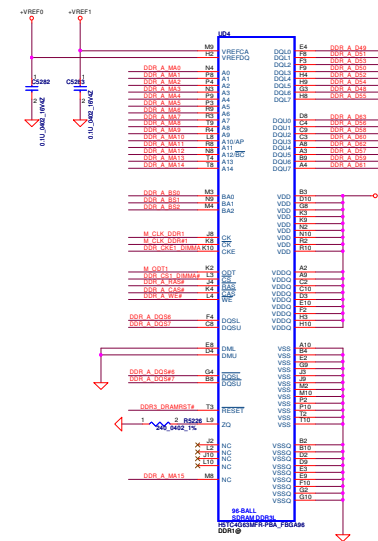
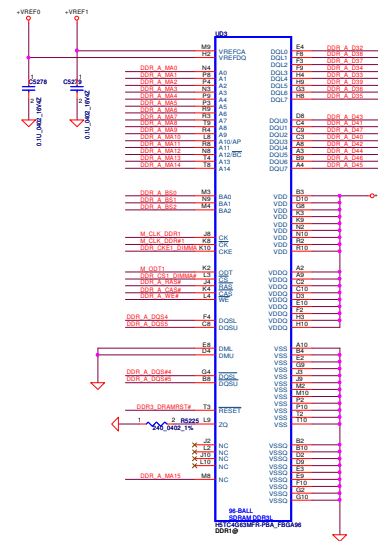
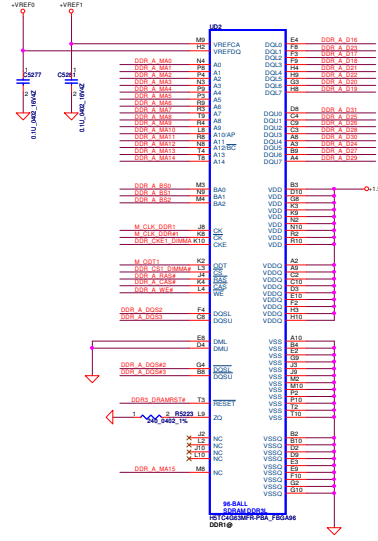
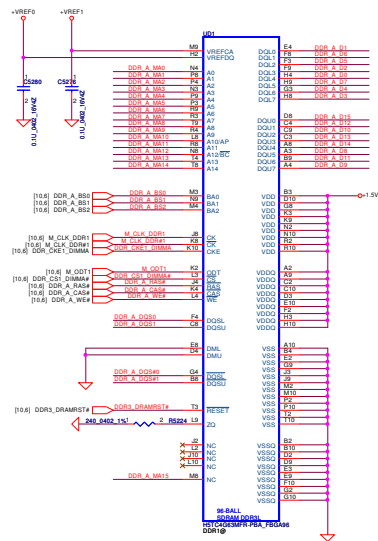


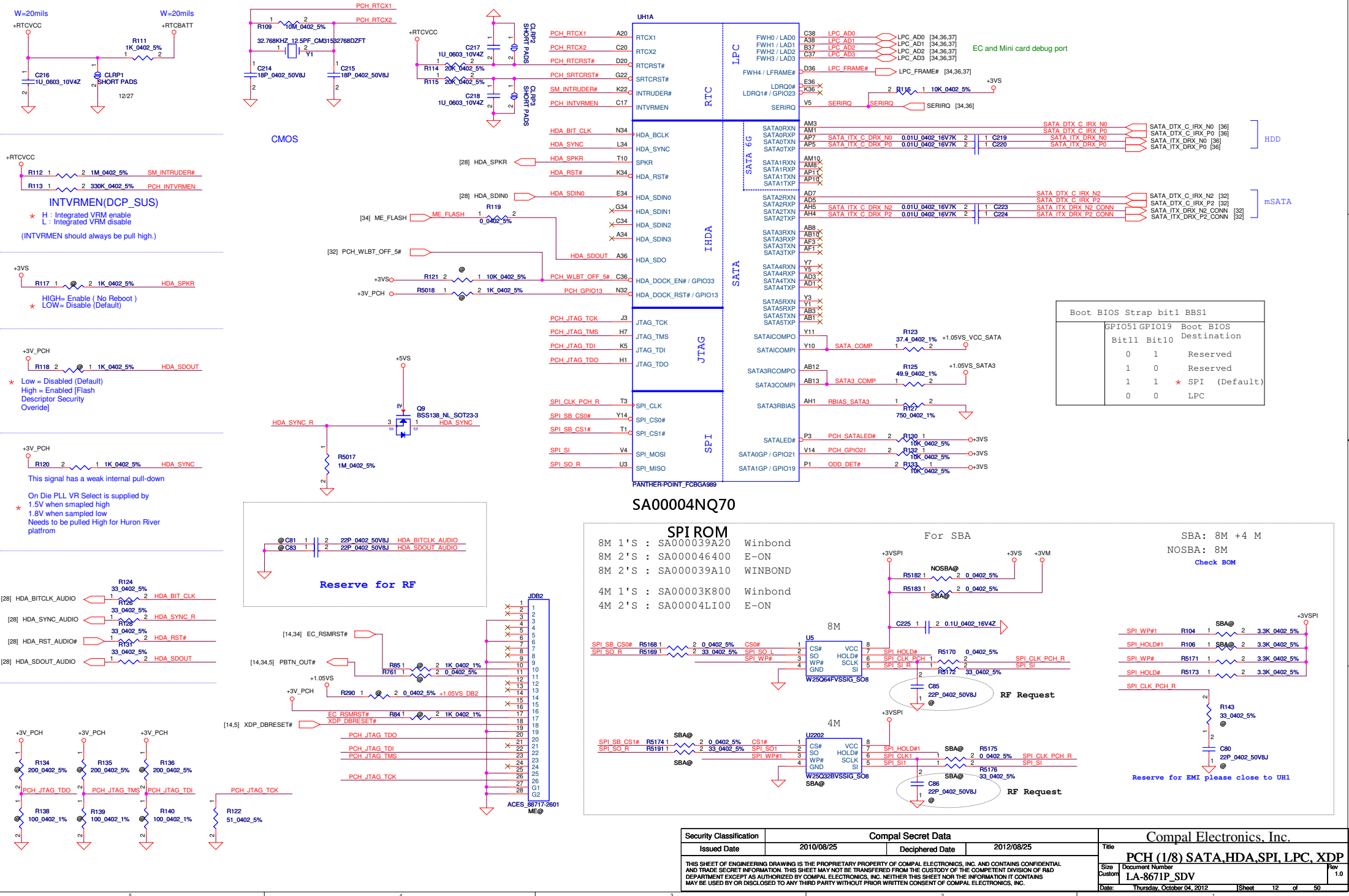
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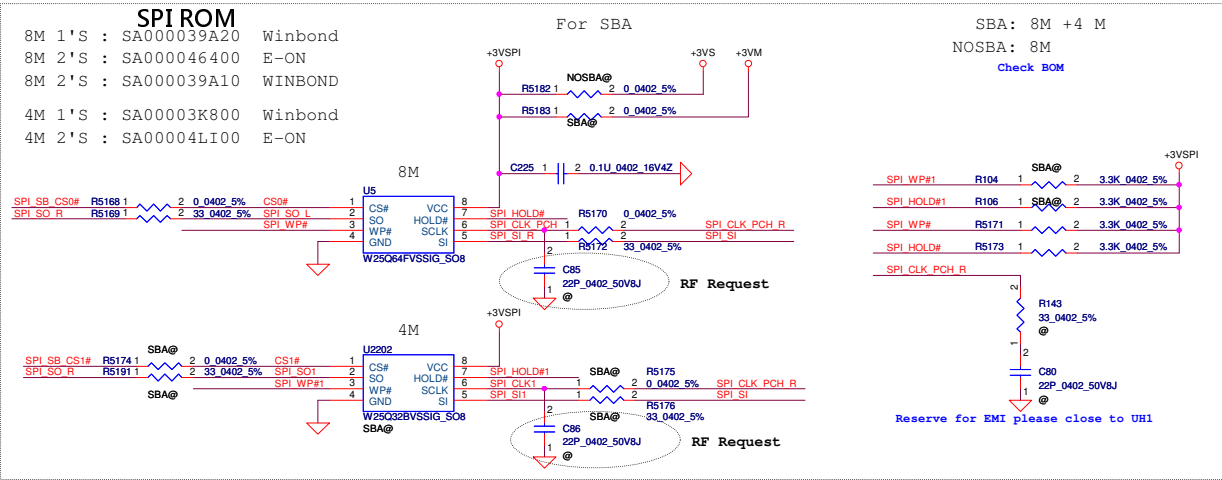
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 [10.6] DDR\_A\_DQ60-7  
 [10.6] DDR\_A\_DQ60-7  
 [10.6] DDR\_A\_DQ60-7

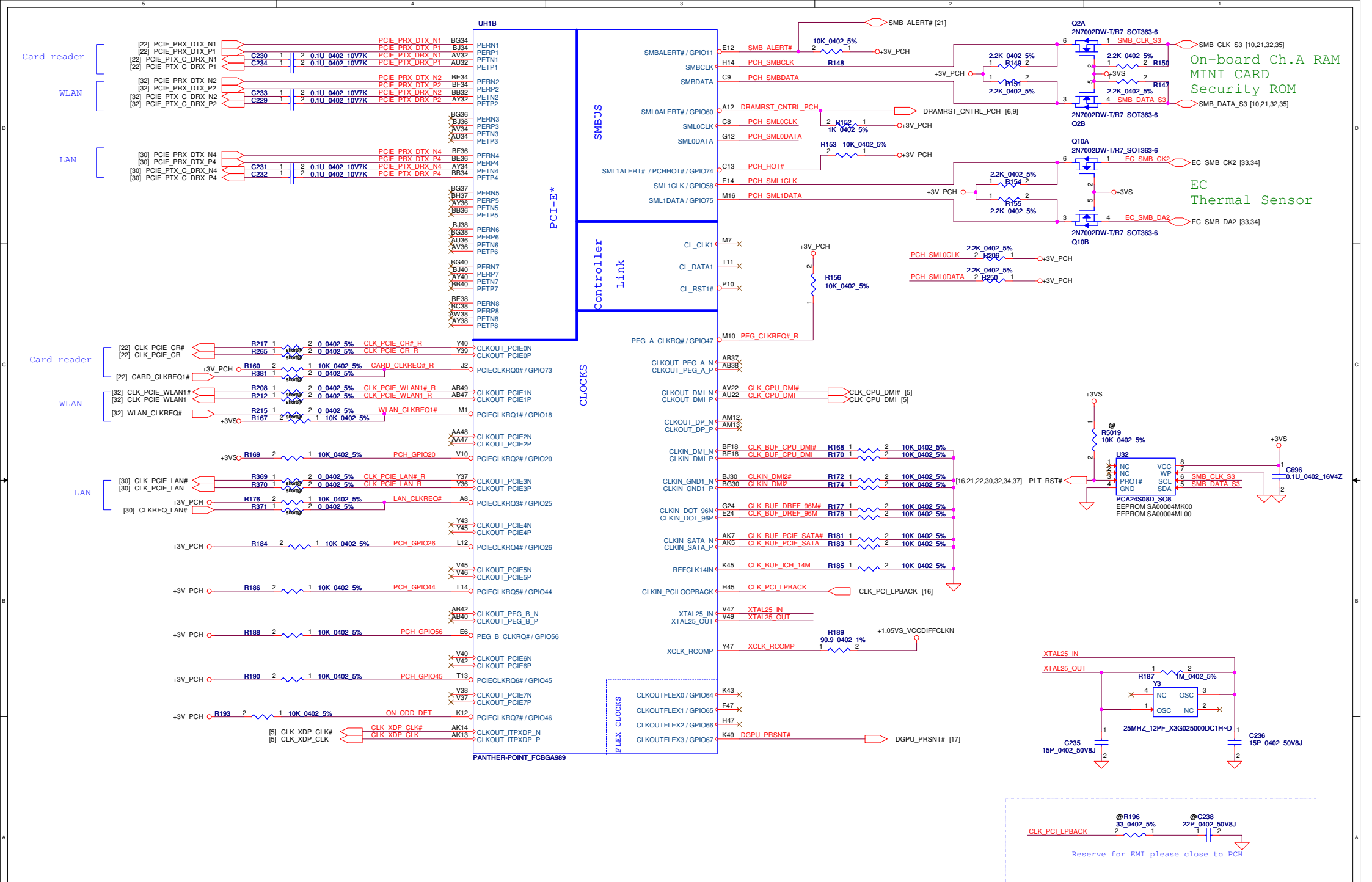




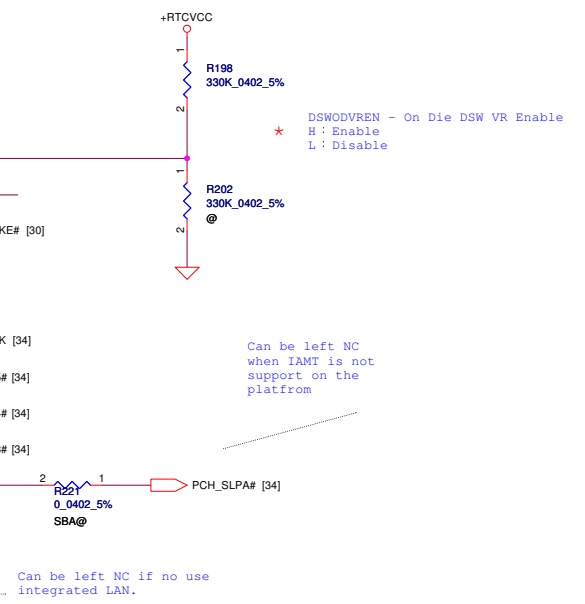
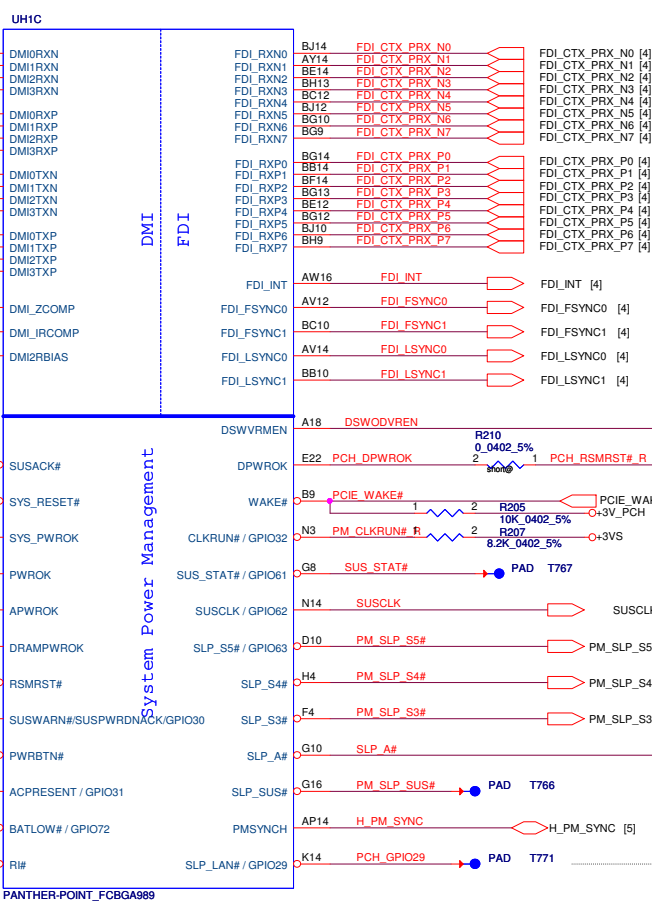
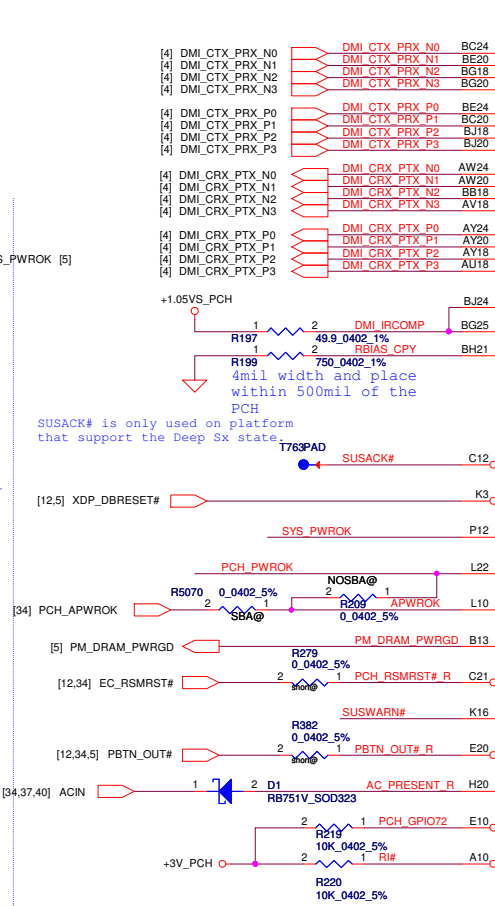
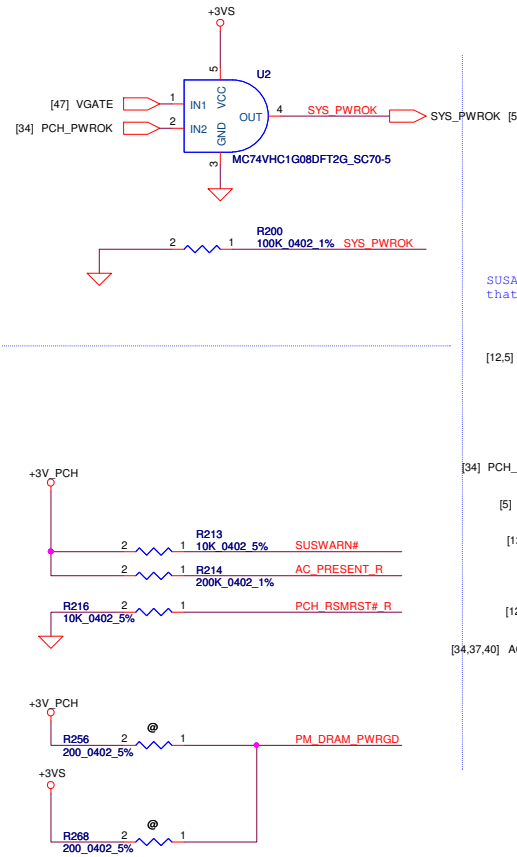
Boot BIOS Strap bit1 BBS1		
GPI051	GPI019	Boot BIOS
Bit11	Bit10	Destination
0	1	Reserved
1	0	Reserved
1	1	★ SPI (Default)
0	0	LPC



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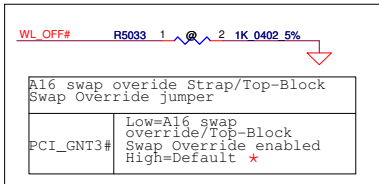
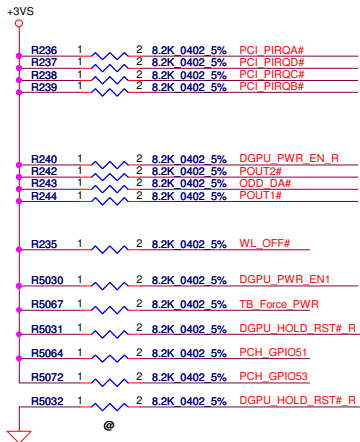
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								Document Number		
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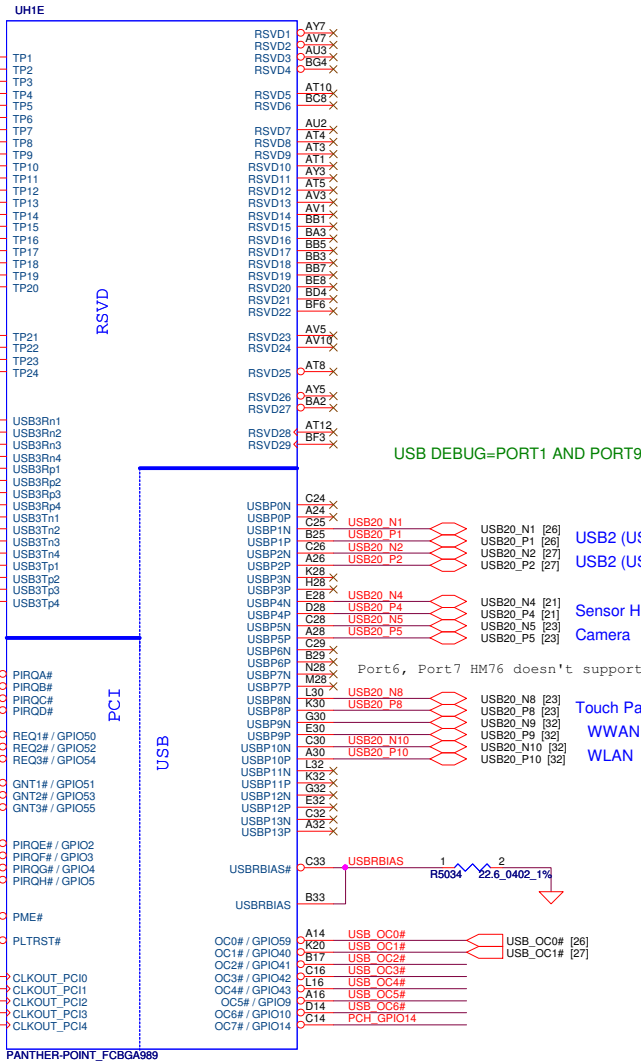
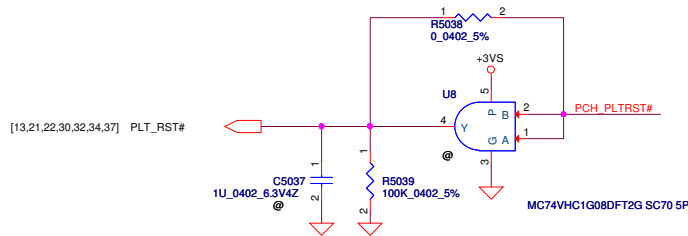
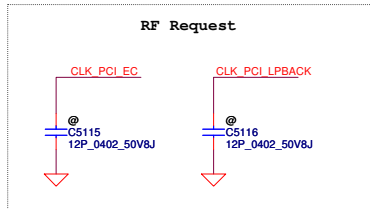
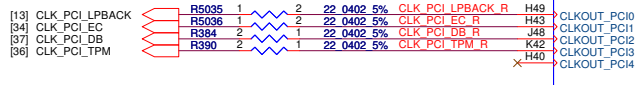
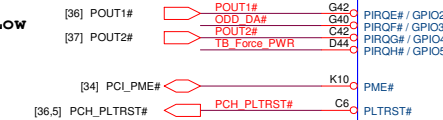
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						3		1		2	







Default: Active Low

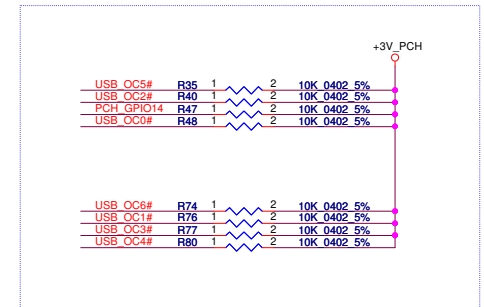


USB DEBUG=PORT1 AND PORT9

USB2 (USB3 COMBO)  
USB2 (USB3 COMBO)

Sensor Hub  
Camera

Touch Panel  
WWAN  
WLAN



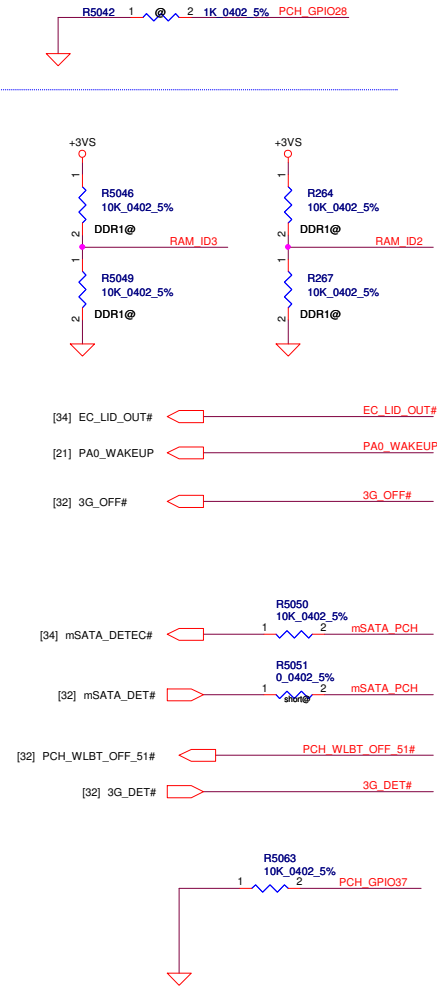
USB3.0	USB2.0	NOTE
1	0	
2	1 *	USB3.0/2.0 Conn
3	2	USB3.0/2.0 Conn
4	3	
	4	Sensor Hub
	5	USB Camera
	6	X
	7	X
	8	Touch Panel
	9 *	WWAN
	10	WLAN
	11	Finger Printer**
	12	
	13	Bluetooth**

\* Debug Port  
\*\* Not Use

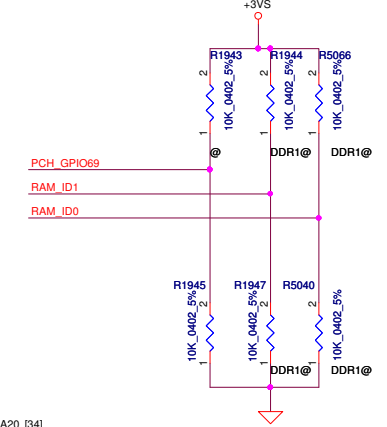
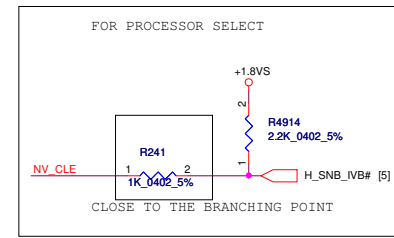
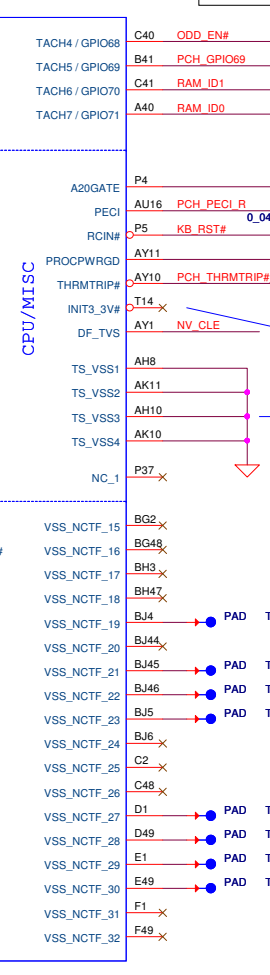
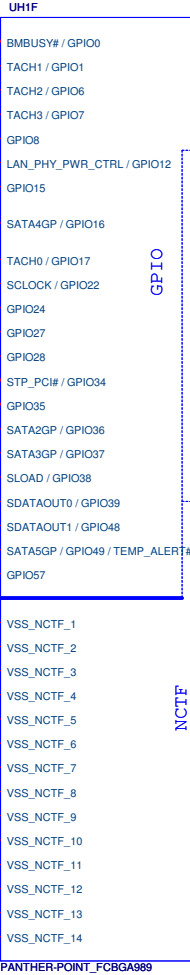
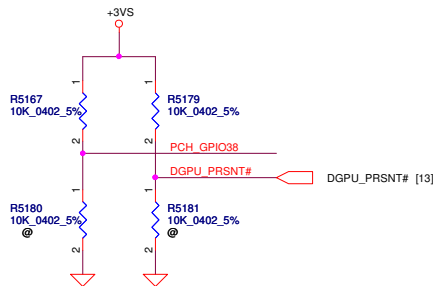
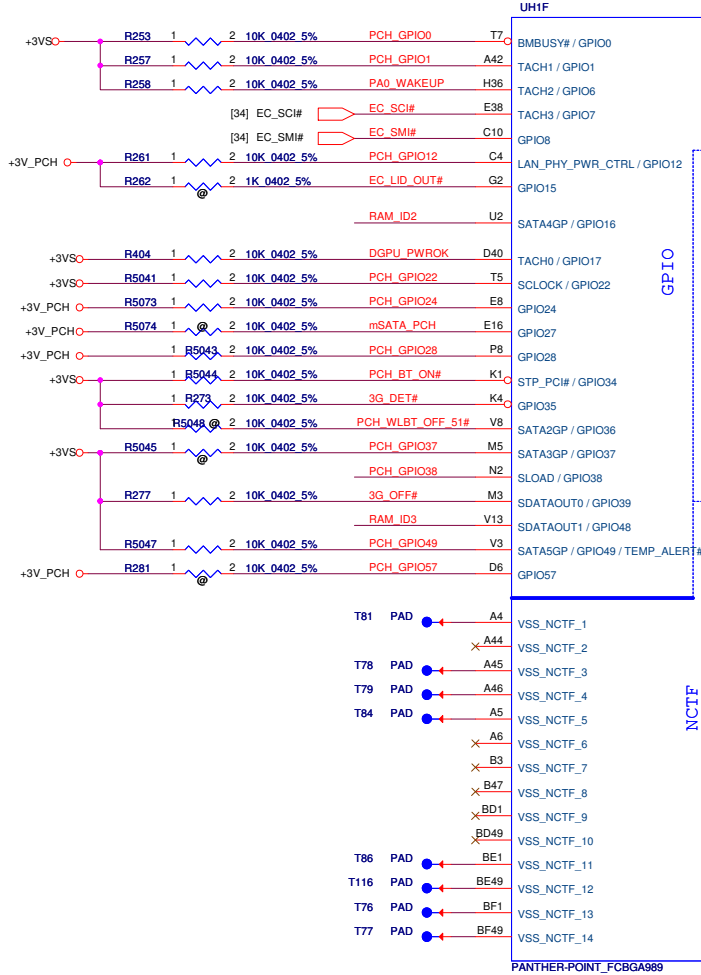
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GPIO28  
On-Die PLL Voltage Regulator  
This signal has a weak internal pull up  
★ H : On-Die voltage regulator enable  
L : On-Die PLL Voltage Regulator disable



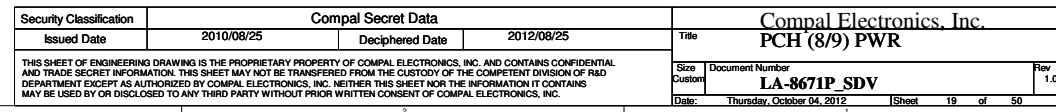
PCH_GPIO69	PCH_GPIO38	PCH_GPIO67	Function	GPU_ID
0	0	0	PX4.0	0
0	0	1	Reserved	1
0	1	0	DIS	2
0	1	1	UMA	3

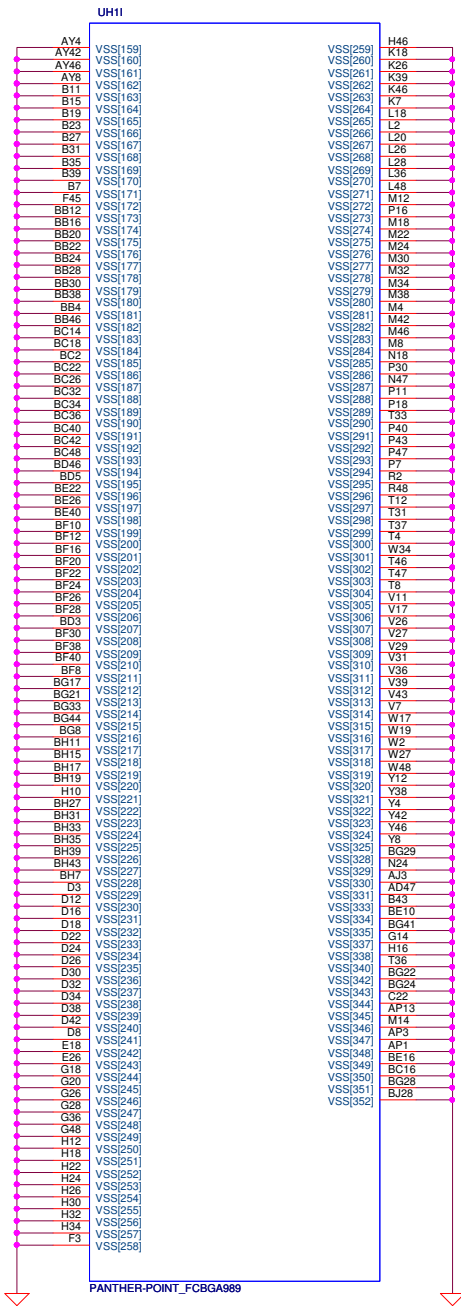
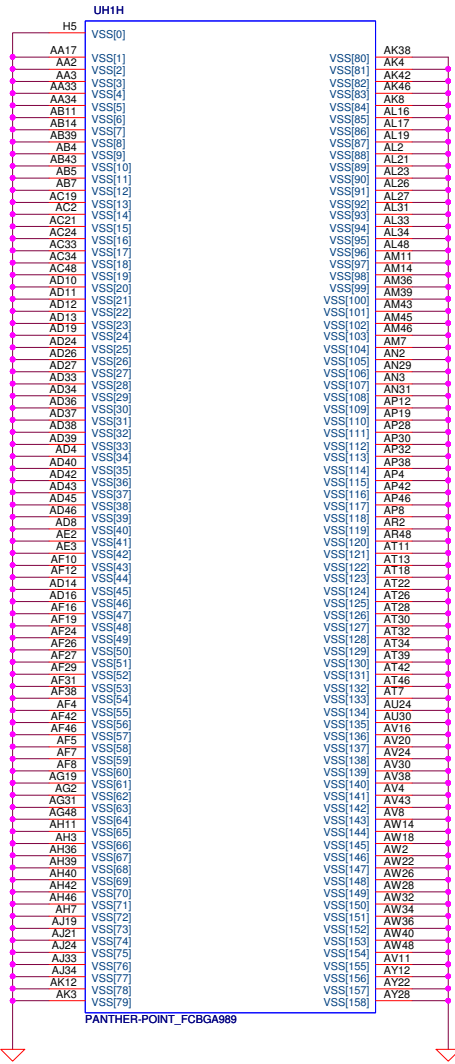


RAM_ID3	RAM_ID2	RAM_ID1	RAM_ID0	RAM
GPIO48	GPIO16	GPIO70	GPIO71	
0	0	0	0	ELPIDA 4GB
0	0	0	1	SAMSUNG 4GB
0	0	1	0	HYNIX 4GB
0	0	1	1	ELPIDA 8GB
0	1	0	0	SAMSUNG 8GB
0	1	0	1	HYNIX 8GB
0	1	1	0	ELPIDA 2GB
0	1	1	1	SAMSUNG 2GB
1	0	0	0	HYNIX 2GB
1	0	0	1	TBD
1	0	1	0	TBD
1	0	1	1	TBD
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1	1	0	1	TBD
1	1	1	0	TBD
1	1	1	1	TBD

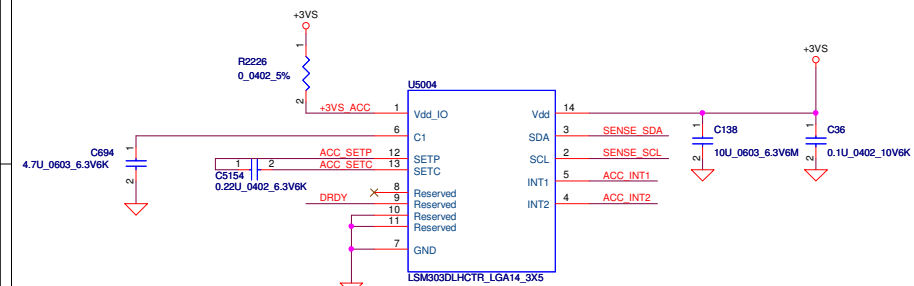
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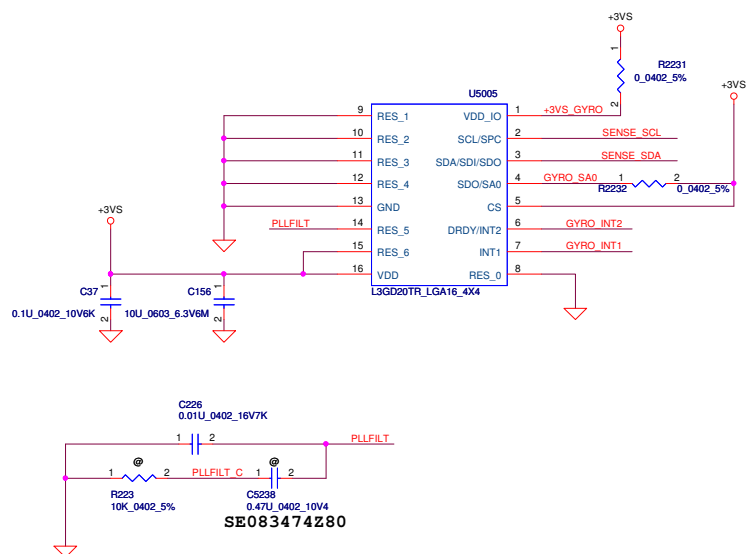




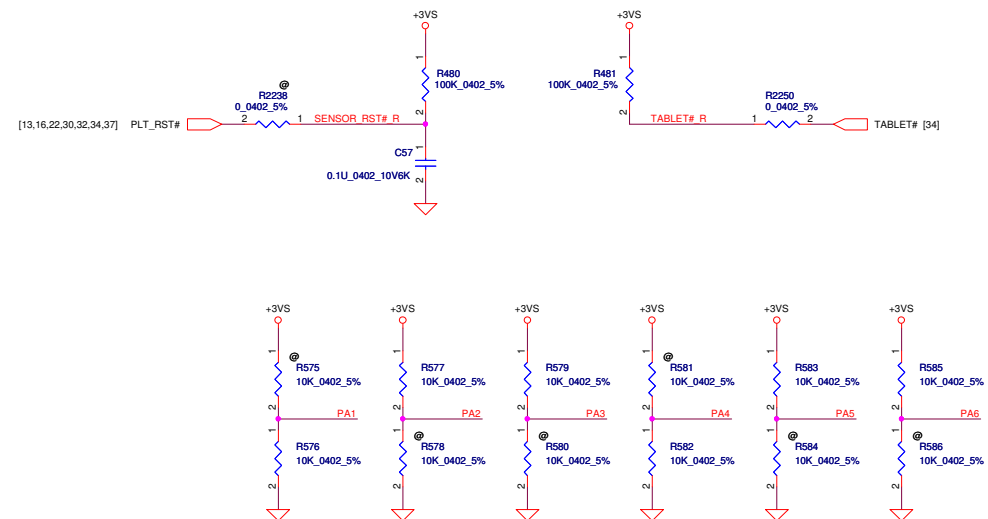
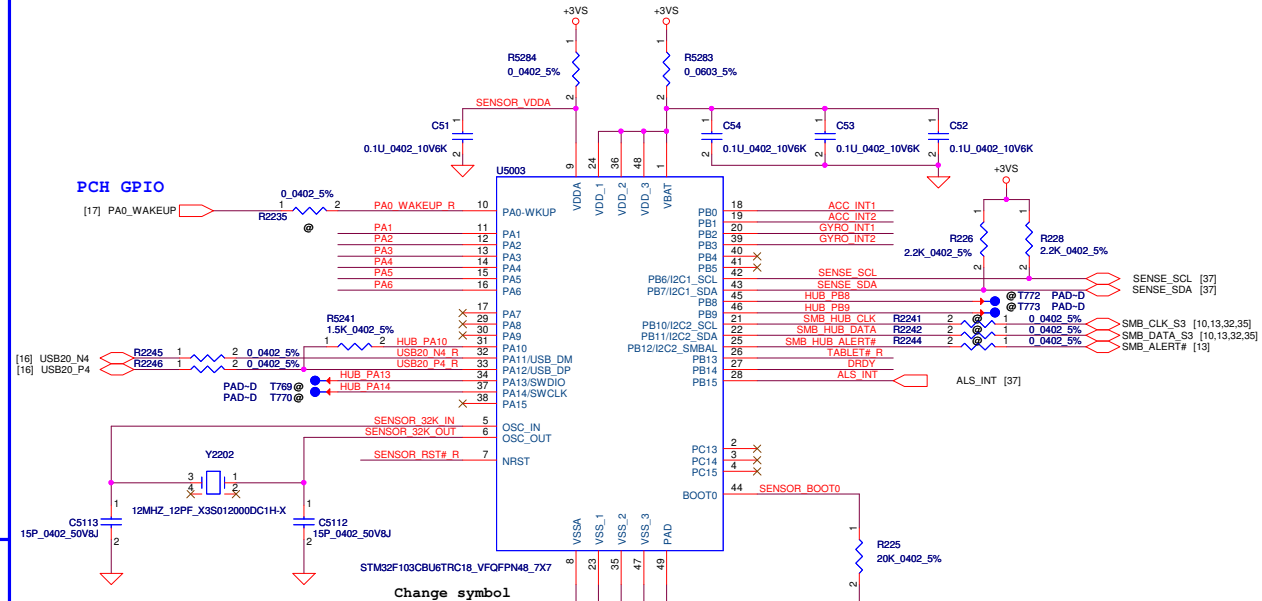
## Accelerometer + eCompass



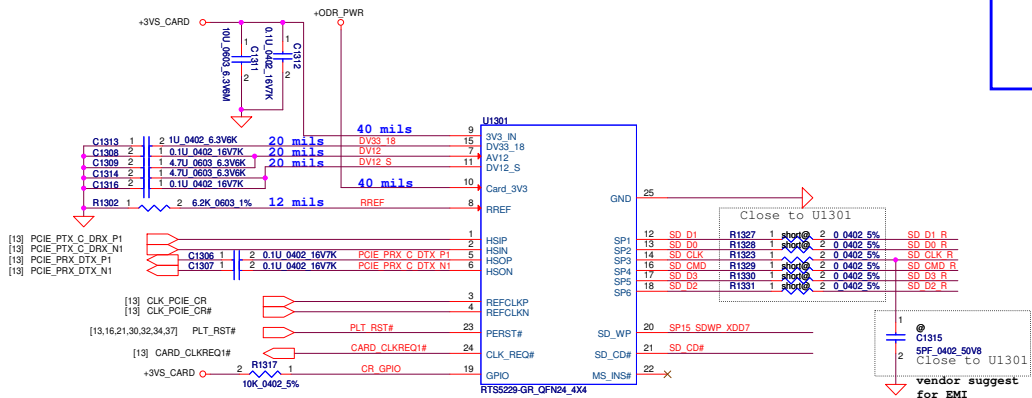
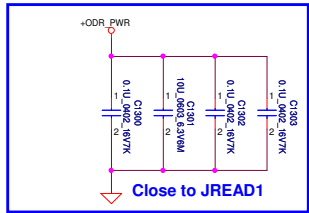
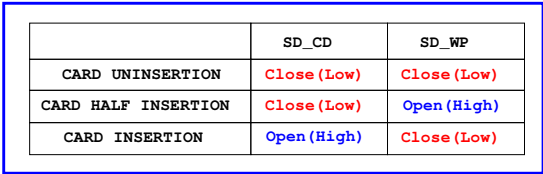
Gyro

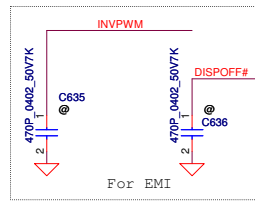


## Sensor Hub



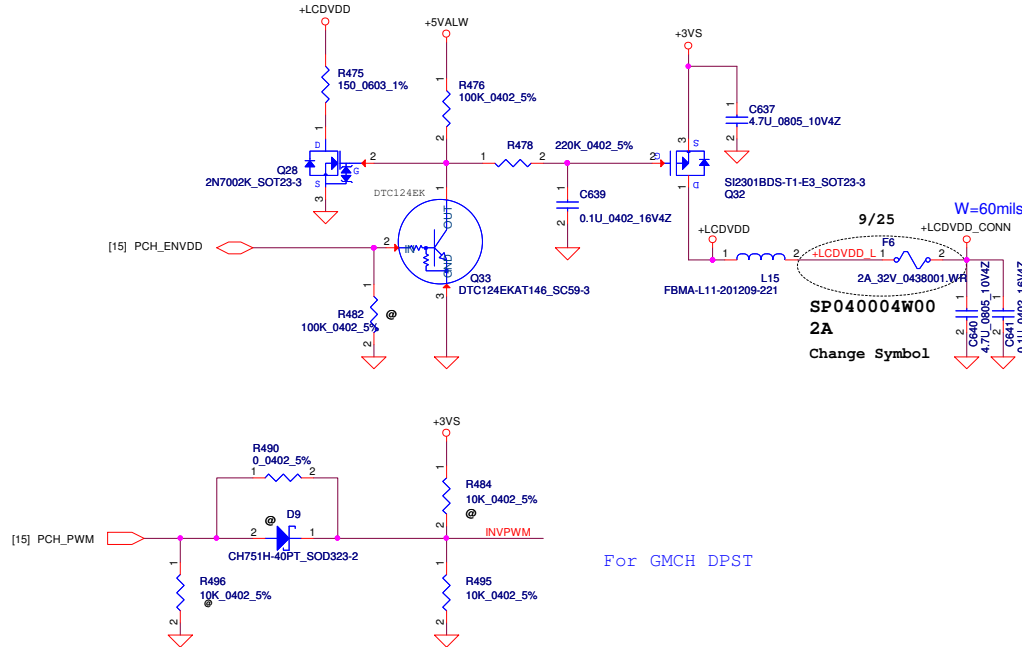
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				Size	Document Number	Rev	
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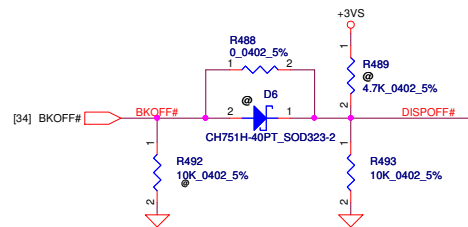


W=60mils

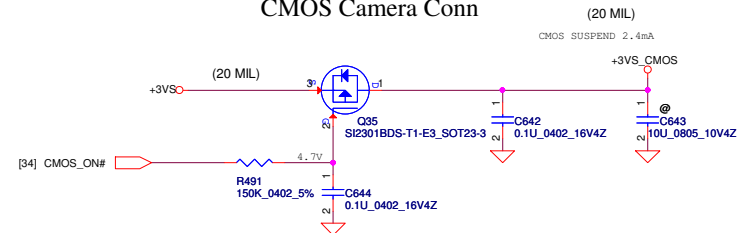
## LCD POWER CIRCUIT



For GMCH DPST

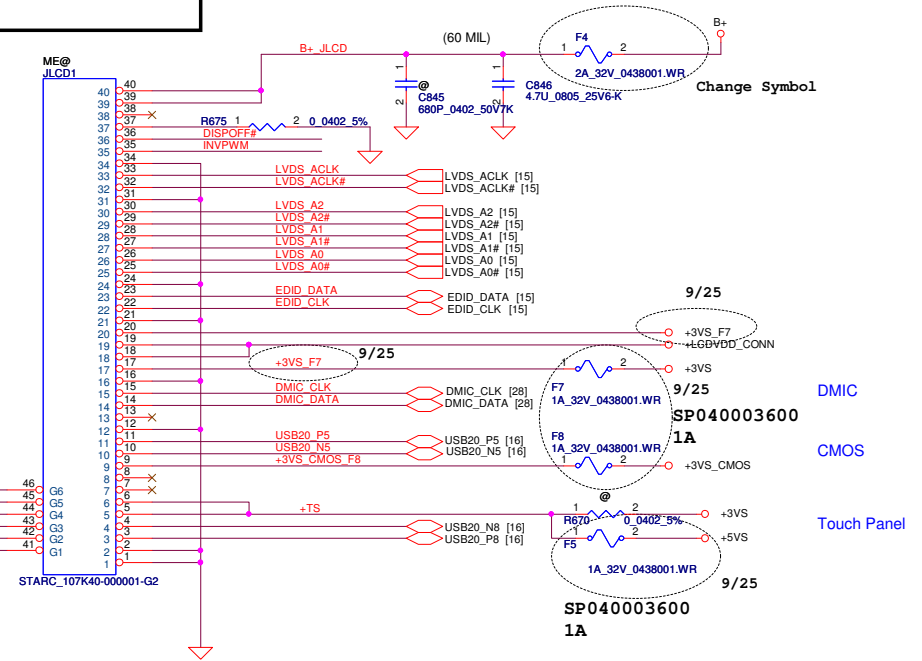


## CMOS Camera Conn



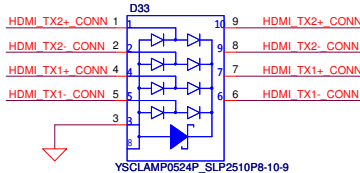
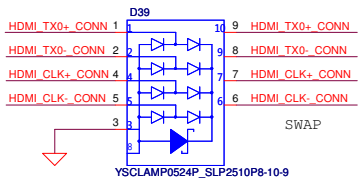
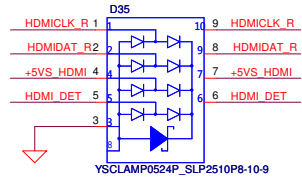
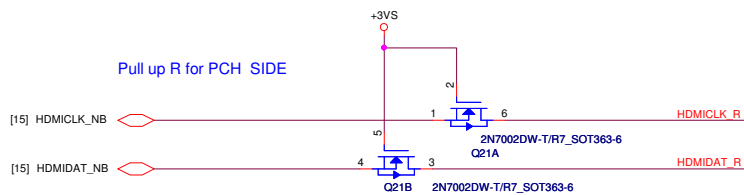
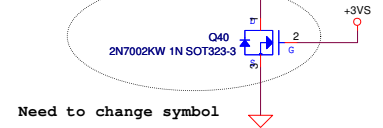
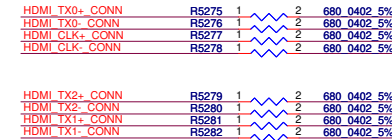
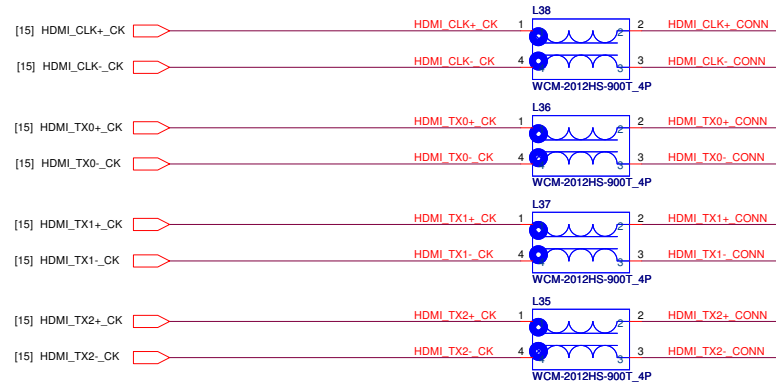
PN:SP01000XE00

SP040004W00  
2A

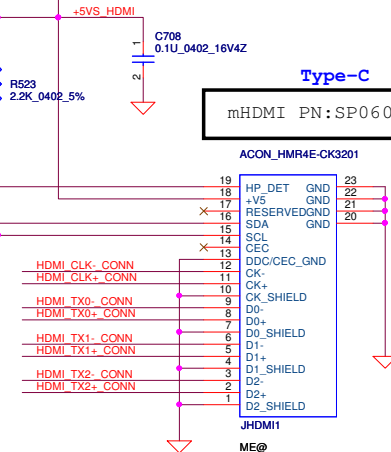
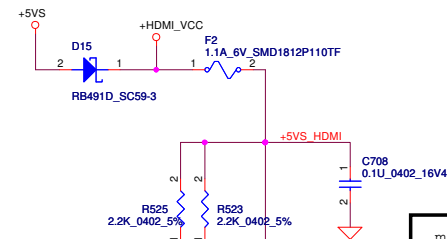
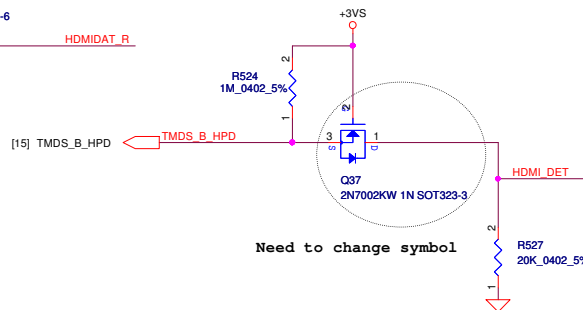


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Size		Document Number		Rev	
Custom		LA-8671P_SDV		1A	
Date:		Thursday, October 04, 2012		Sheet 23 of 50	

# PN:SM070001S00x 4



1'SPN: SC300001Y00  
2'SPN: SC300002800

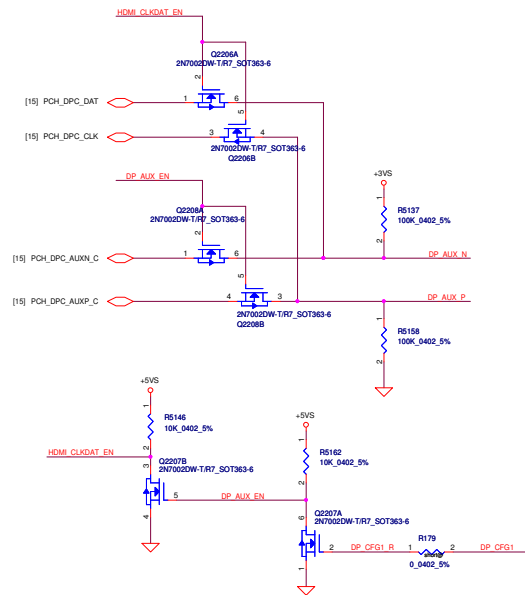
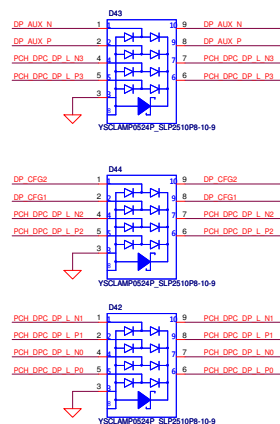
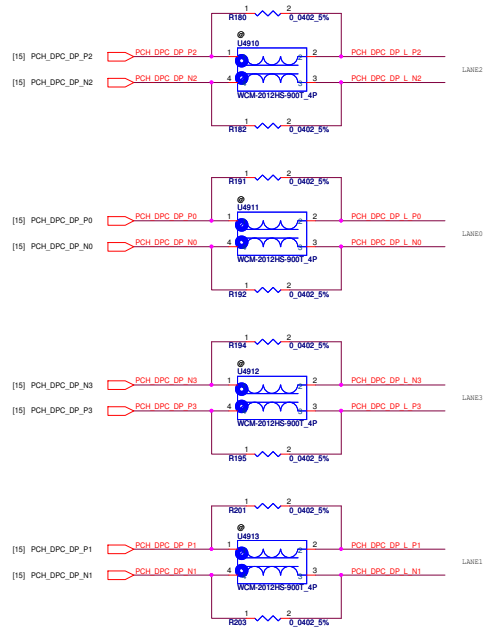


Type-C  
mHDMI PN:SP060005H00

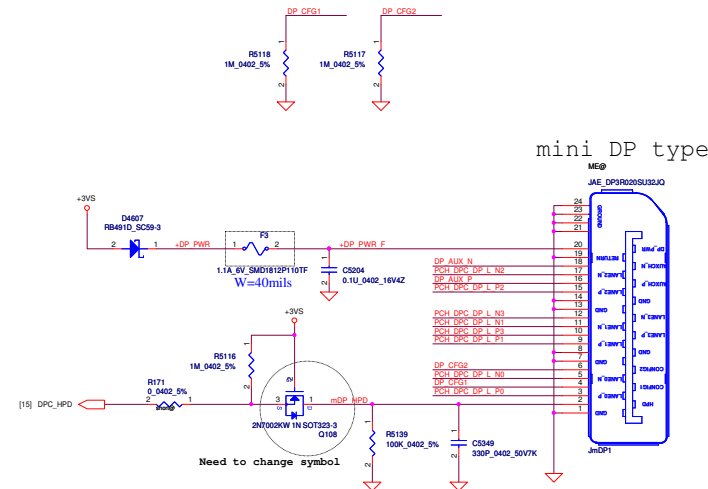
Security Classification		Compal Secret Data				Compal Electronics, Inc.					
Issued Date		2010/08/25		Deciphered Date		2012/08/25		Title			
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						Size		Document Number		Rev	
						Custom		LA-8671P_SDV		1.0	
						Date:		Thursday, October 04, 2012		Sheet 24 of 50	



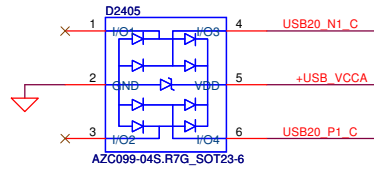
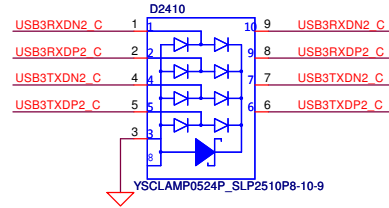
Close to JmDP1



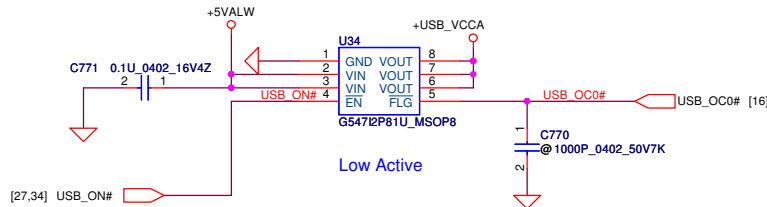
HPD	CFG1	CFG2	LSRX	Mode
1	0	0	X	DP
1	1	X	X	HDMI
0	0	1	1	TBT



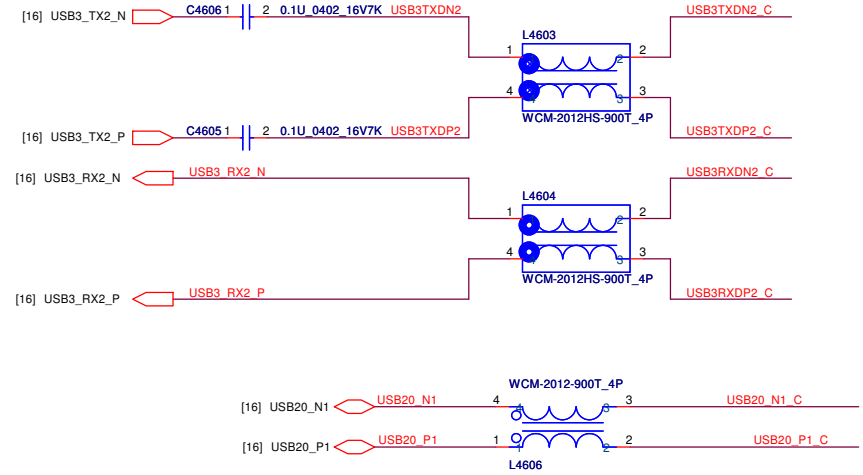
1'SPN: SC300001Y00  
2'SPN: SC300002800



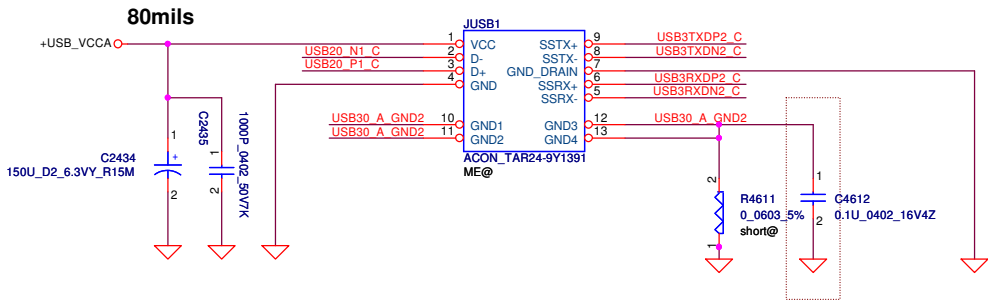
1'S PN: SC300001G00  
2'S PN: SC300002E00



## JUSB1USB3.0/USB2.0

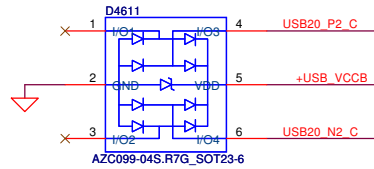
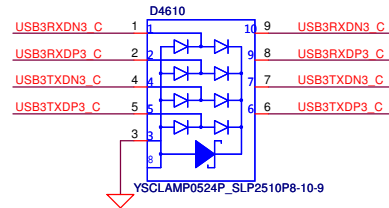


## DC23300A410

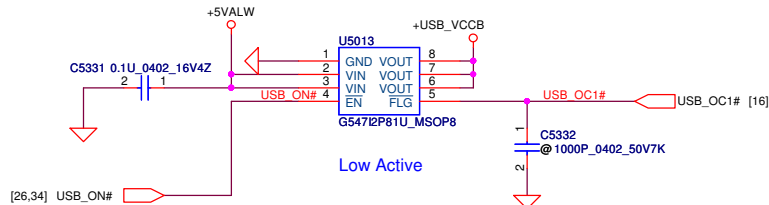


Security Classification				Compal Secret Data				Compal Electronics, Inc.			
Issued Date				2011/05/16		Deciphered Date		2013/05/16		Title	
										USB3.0/2.0 CONN-L	
										LA-8671P_SDV	
										Rev 1.0	
										Date: Thursday, October 04, 2012	
										Sheet 26 of 50	

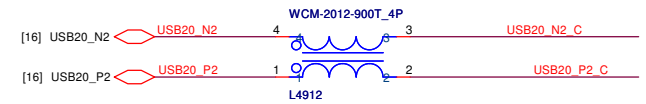
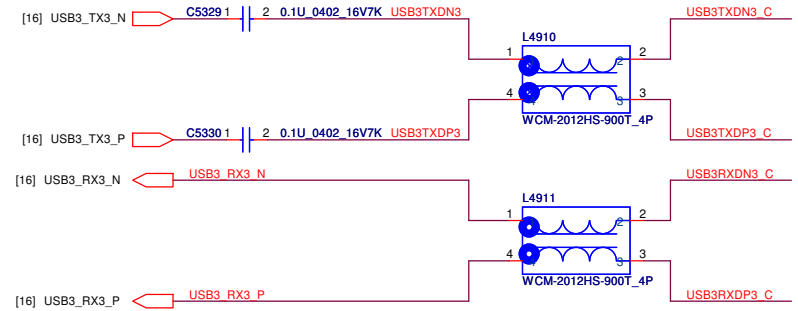
1'SPN: SC300001Y00  
2'SPN: SC300002800



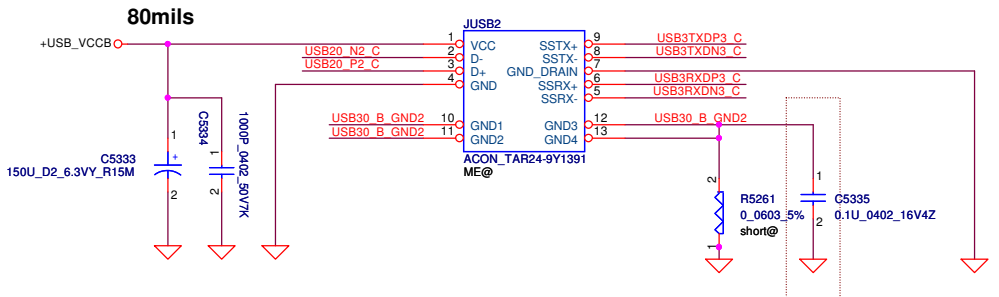
1'S PN: SC300001G00  
2'S PN: SC300002E00



## JUSB1USB3.0/USB2.0



DC23300A410

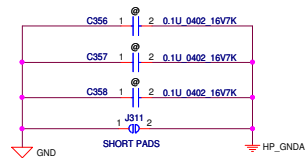
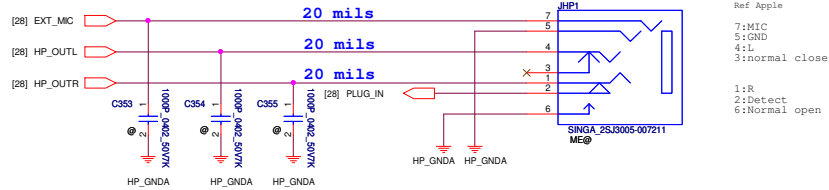


Security Classification				Compal Secret Data				Compal Electronics, Inc.			
Issued Date				2011/05/16		Deciphered Date		2013/05/16		Title	
										USB3.0/2.0 CONN-R	
										Size	
										Document Number	
										LA-8671P_SDV	
										Date	
										Thursday, October 04, 2012	
										Sheet	
										27	
										of	
										50	
										Rev	
										1.0	

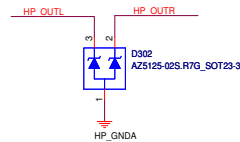


PN:DC230008E00

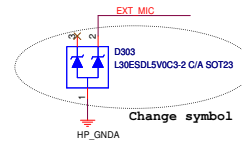
Choose normal-open type!



GND



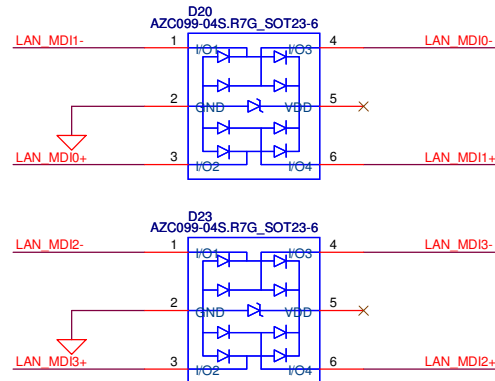
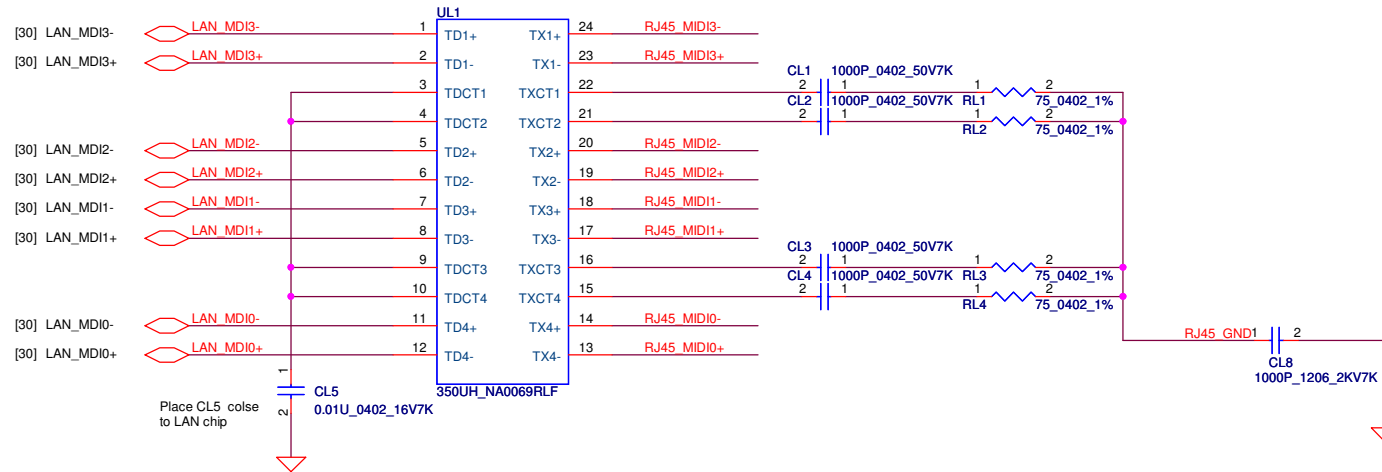
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PN: SCA00001L00

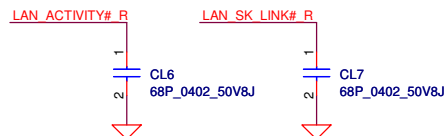
Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2011/05/16	Deciphered Date	2013/05/16	Title	
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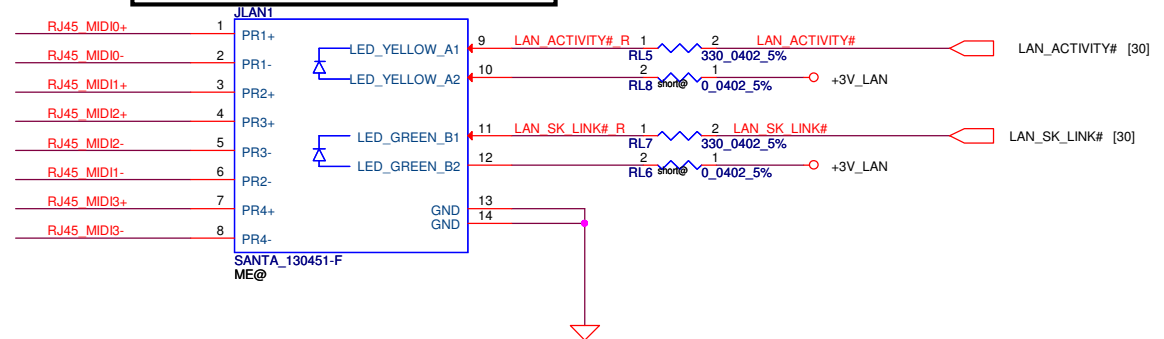
D20/D23  
1'S PN:SC300001G00  
2'S PN:SC300002E00

For EMI's request



DC234005300  
LAN Conn.

RL5,RL7 Vendor recommend 510 ohm

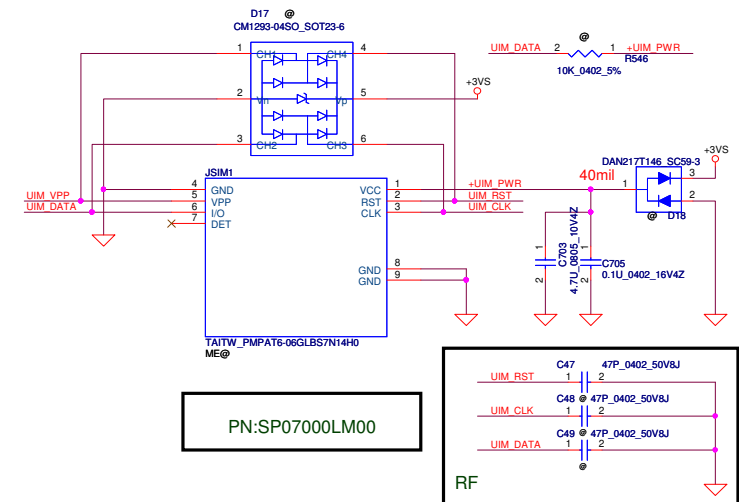


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						Size Custom	Document Number			LA-8671P_SDV		Rev 1.0
						Date:	Thursday, October 04, 2012		Sheet	31	of	50

## Mini-Express Card(WLAN/BT Combo)

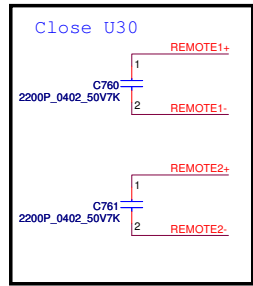


Close to JMIN2 on the same side

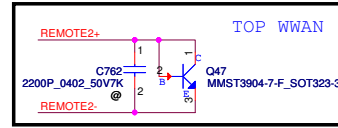
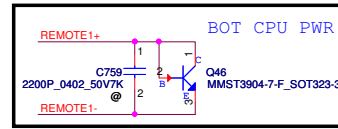
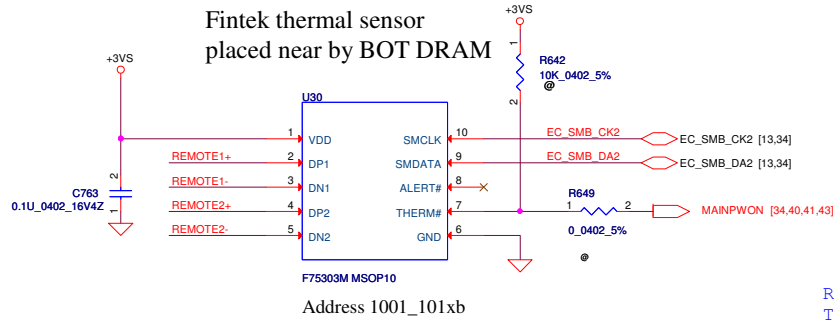


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Issued Date	2010/08/25	Deciphered Date	2012/08/25	Title	WLAN/BT/WWAN/m-SATA
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				Custom LA-8671P_SDV	1.0
Date:		Thursday, October 04, 2012		ISheet	32 of 50



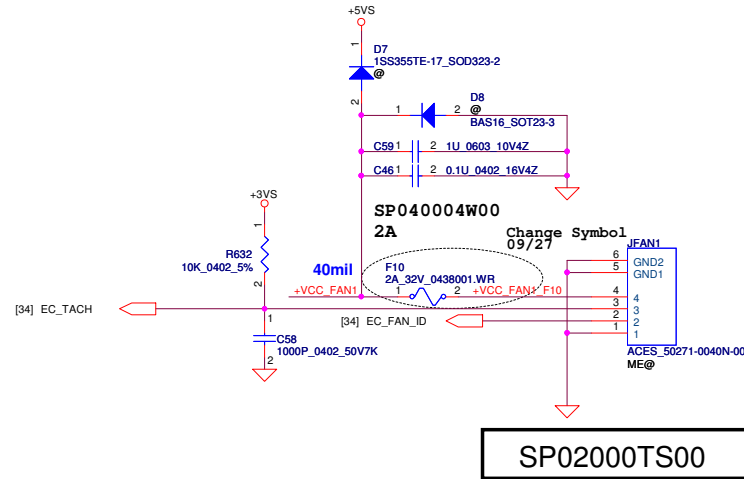
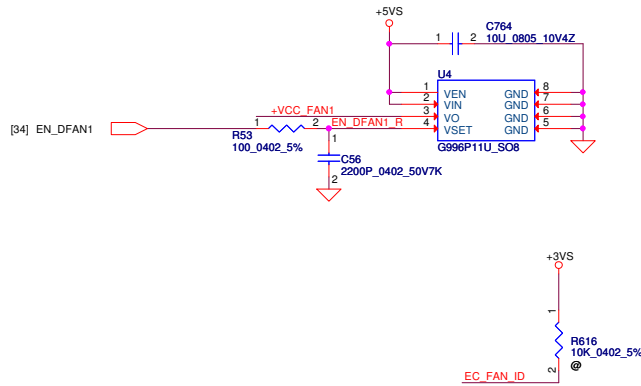


Fintek thermal sensor  
placed near by BOT DRAM

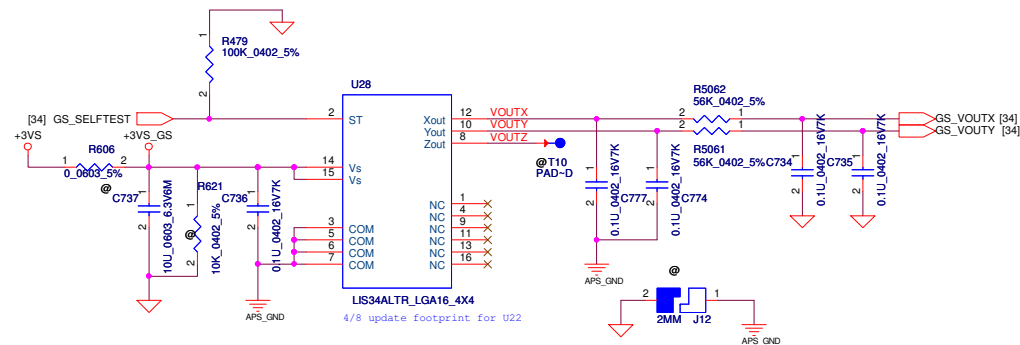
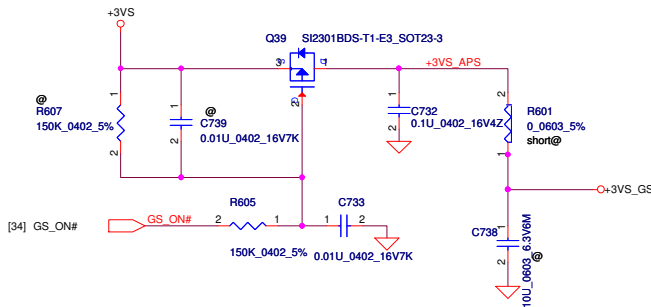


REMOTE1,2+/-:  
Trace width/space:10/10 mil  
Trace length:<8"

FAN +5VS DROOP



APS G-Sensor



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Diagram illustrating the pin connections for the 100P\_0402\_50V8J component:

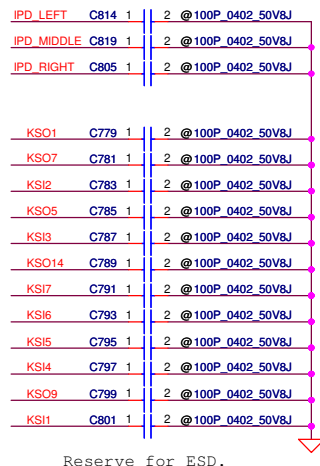
- KSI[0..7]** (Blue line) connects to **KSI[0..7]** [34]
- KSO[0..15]** (Blue line) connects to **KSO[0..15]** [34]

Pin connections for the 100P\_0402\_50V8J component:

Pin Label	Pin Number	Component Pin
KSO2	C778	2 @100P_0402_50V8J
KSO15	C780	2 @100P_0402_50V8J
KSO6	C782	2 @100P_0402_50V8J
KSO8	C784	2 @100P_0402_50V8J
KSO13	C786	2 @100P_0402_50V8J
KSO12	C788	2 @100P_0402_50V8J
KSO11	C790	2 @100P_0402_50V8J
KSO10	C792	2 @100P_0402_50V8J
KSO3	C794	2 @100P_0402_50V8J
KSO4	C796	2 @100P_0402_50V8J
KSO	C798	2 @100P_0402_50V8J
KSO0	C800	2 @100P_0402_50V8J

CONN PIN define need double check

CONN PIN define need double check



**JAE FL45030HA3R3000A-DT**

**ME@**

**JKB1**

Pin	Function
1	KS11
2	KS17
3	KS18
4	KSO9
5	KS14
6	KS15
7	KSO0
8	KS12
9	KS13
10	KSO5
11	KSO1
12	KS10
13	KSO2
14	KS04
15	KS07
16	KS08
17	KS06
18	KSO3
19	KSO12
20	KS013
21	KS014
22	KS011
23	KS010
24	KS015
25	
26	IPD LEFT
27	IPD MIDDLE
28	IPD RIGHT
29	
30	
31	GND1
32	GND2

M1(Left BUTTON)  
M2(Center BUTTON)  
M3(Right BUTTON)

PN:SP01001CH00

09/27

SP040003600

1A

F9  
1A\_32V\_0438001.WR

+5VSO 1 2 +5VS\_F9

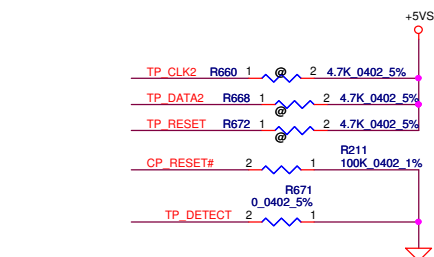
[34] TP\_RESET

JTP2

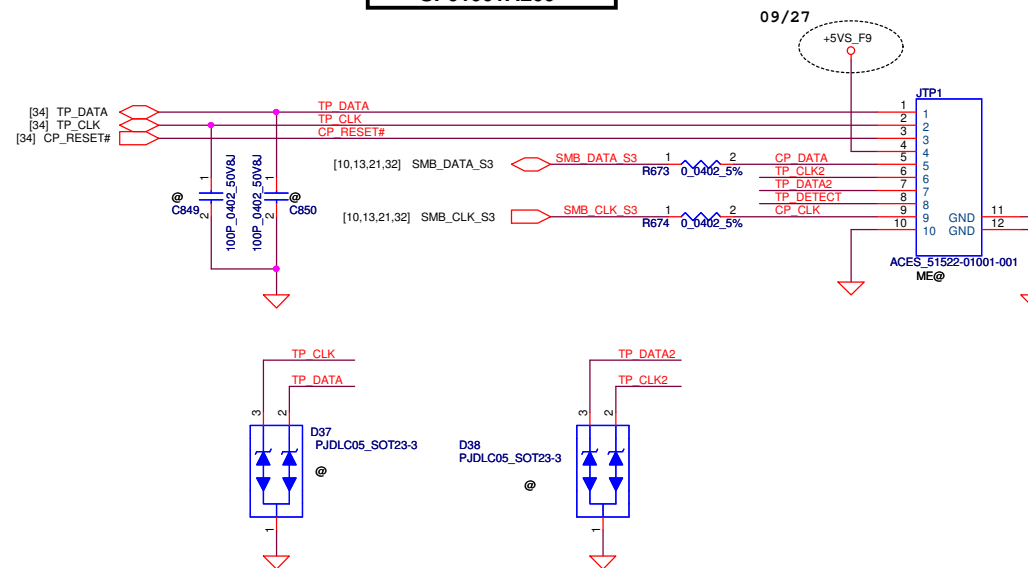
1  
2  
3  
4  
5  
6  
7  
8  
9  
10  
11  
12

GND  
GND

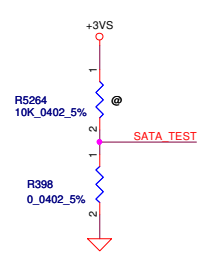
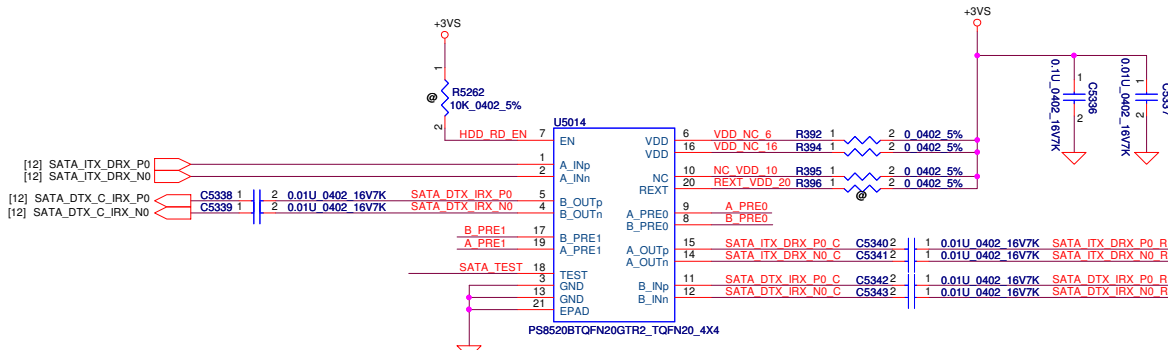
ACES\_50524-0100N-001  
ME@



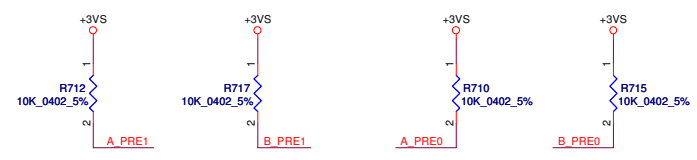
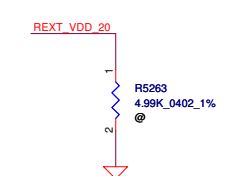
SP01001AL00



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				Date Thursday, October 04, 2012	Sheet 35 of 50

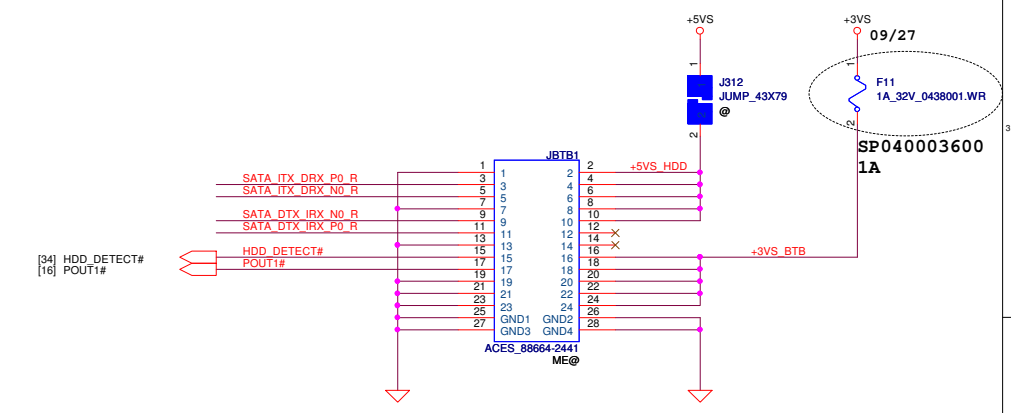
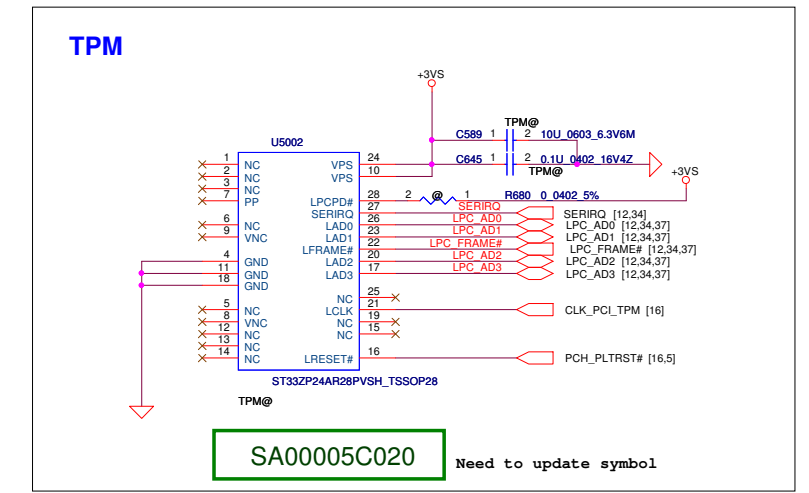


	TEST (Internal Low)
Normal Mode (Default)	LOW
Test Mode	HIGH

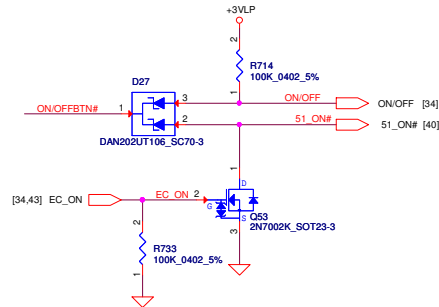
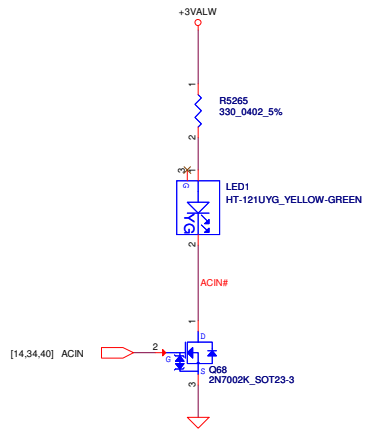


Remove pull-low

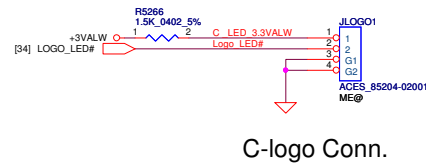
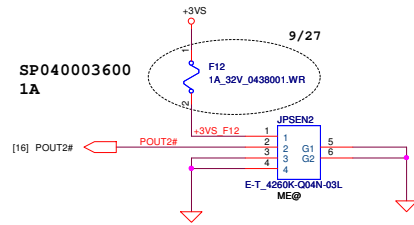
	A_PRE1/B_PRE1 (Internal pull Low)	A_PRE0/B_PRE0 (Internal pull Low)
0dB, no pre-emphasis	Low	Low
1.5dB pre-emphasis is selected	Low	High
2.5dB pre-emphasis is selected	High	Low
3.5dB pre-emphasis is selected	High	High



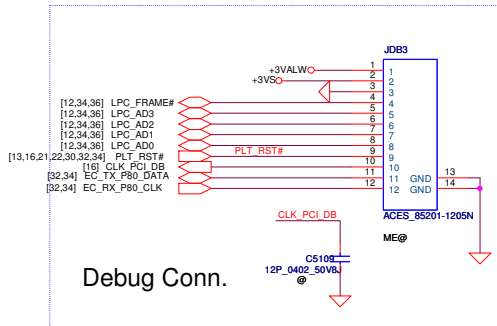
## AC-IN LED (Color : Green)



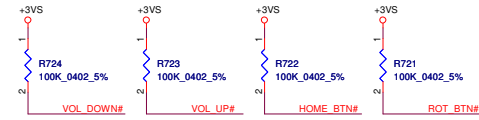
SP040003600  
1A



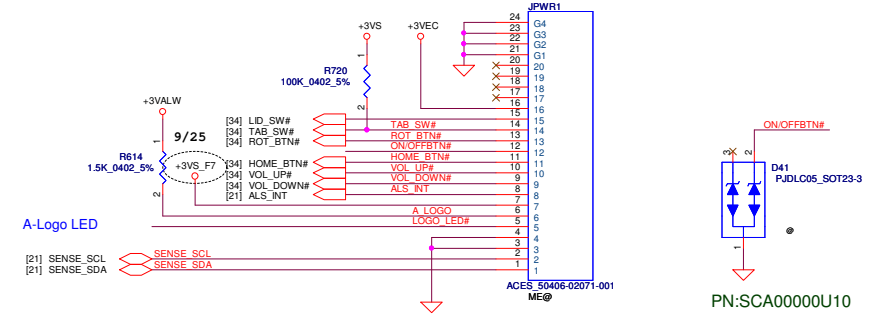
C-Logo Conn.



Debug Conn.

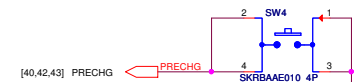


SP010016L00



Power Board connector

BOTTOM SIDE  
SN111005800 by ME drawing

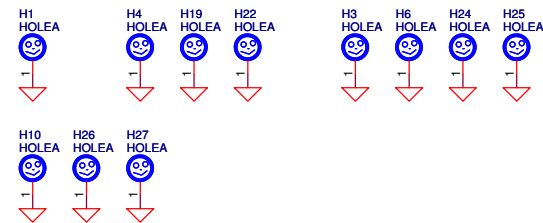


For Power Reset

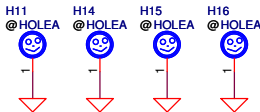
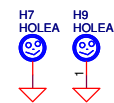
Security Classification		Compal Secret Data		Compal Electronics,Ltd.	
Issued Date	2010/08/25	Deciphered Date	2012/08/25	other IO connector	
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				Date: Thursday, October 04, 2012	Rev 1A

PT-H 20 SCREW HOLE

H\_2P3



H\_3P3



H\_6P0

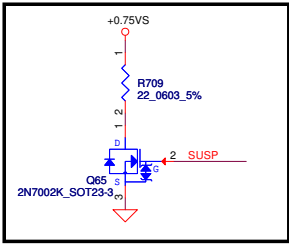
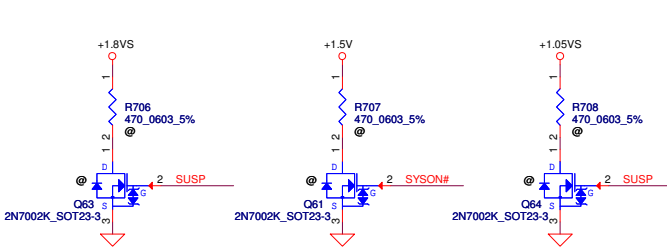
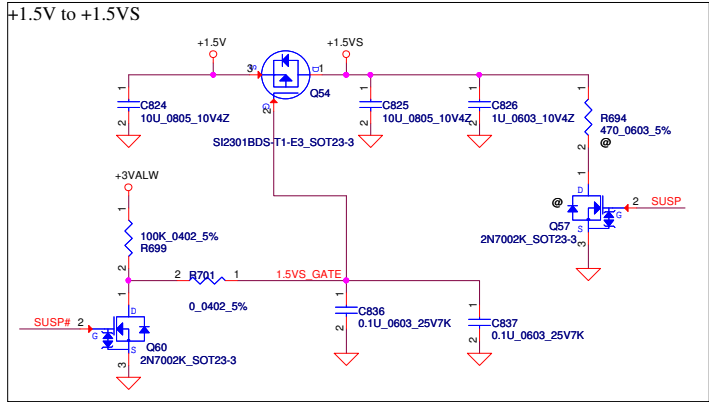
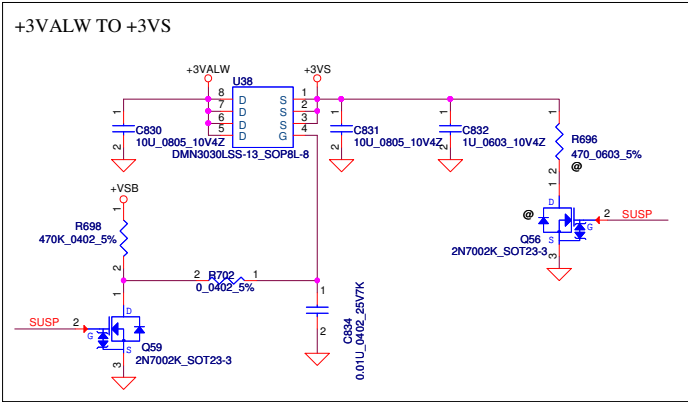
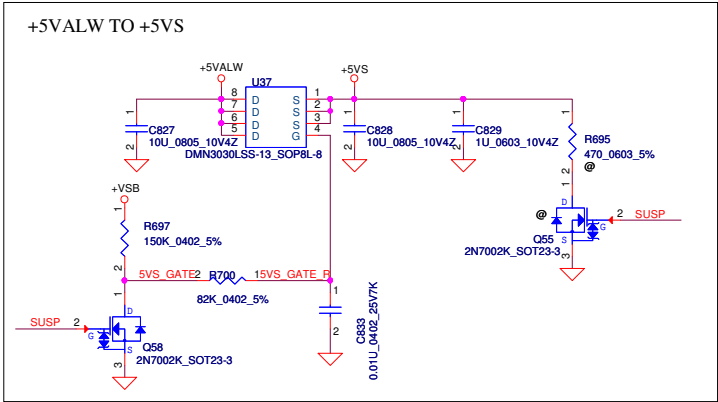


NON-PT-H 1 SCREW HOLE

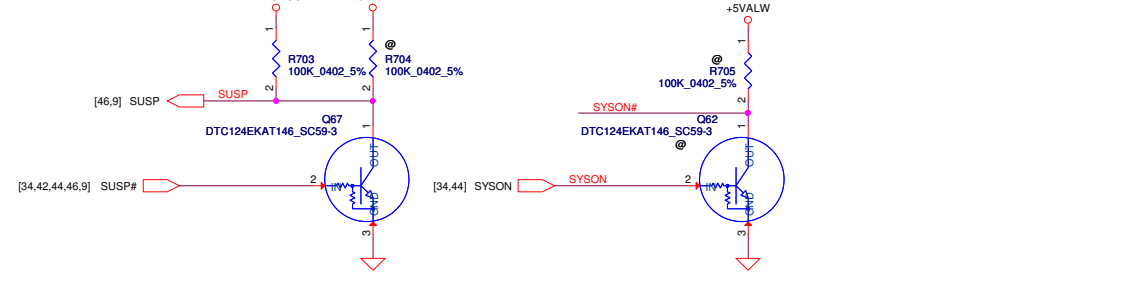
H\_2P4N



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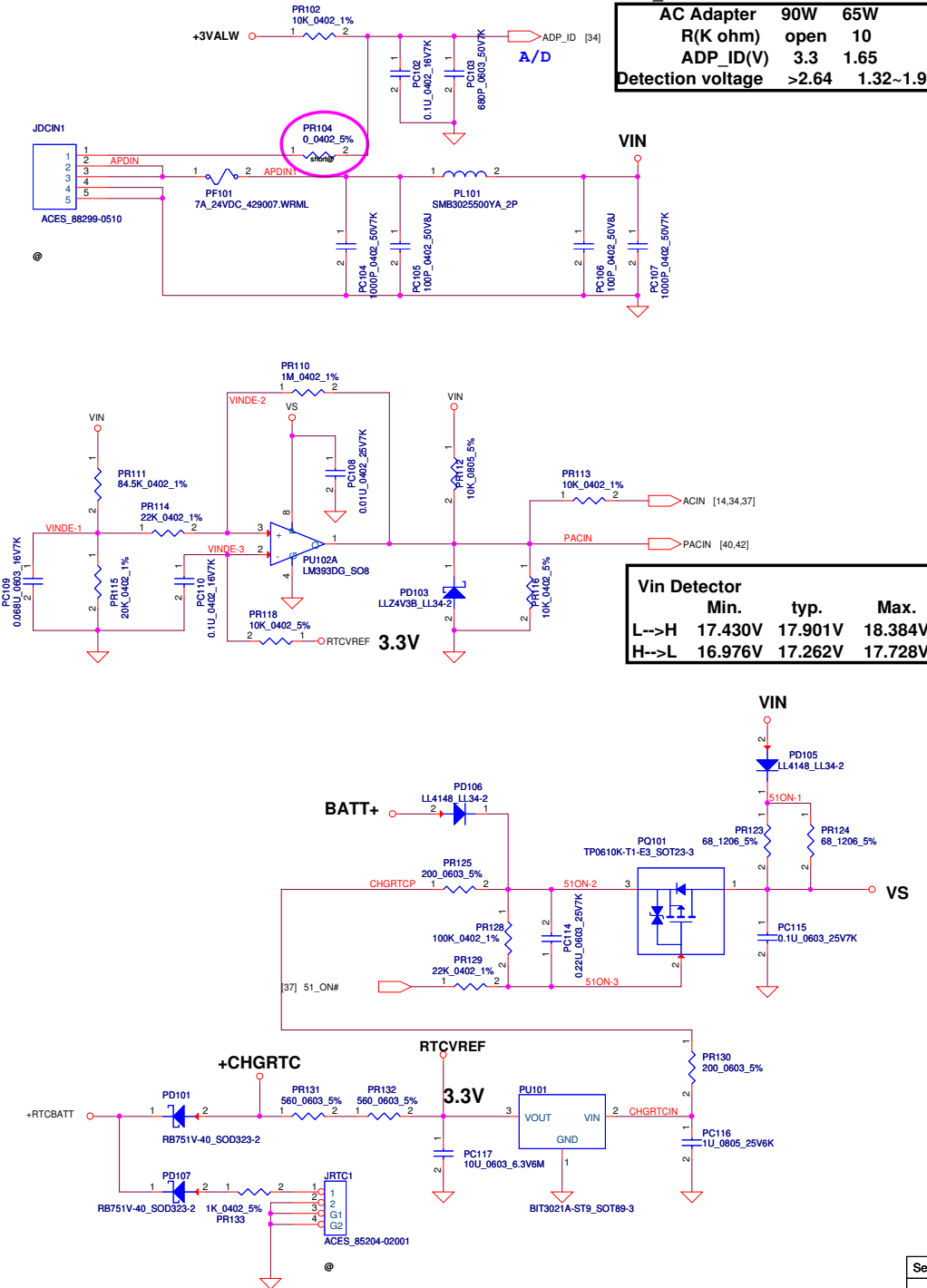
For Intel S3 Power Reduction.



# ADP\_ID

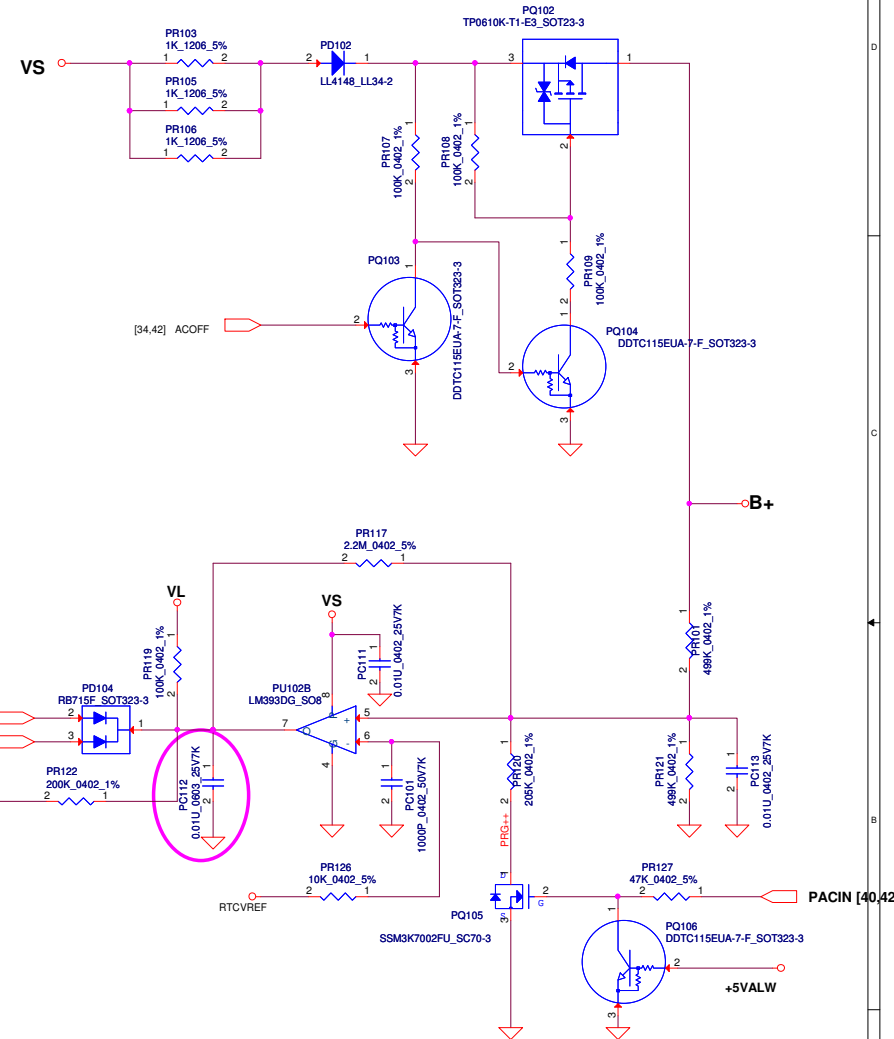
AC Adapter	90W	65W
R(K ohm)	open	10
ADP_ID(V)	3.3	1.65
Detection voltage	>2.64	1.32~1.98

# Precharge detector 15.97V/14.84V FOR ADAPTOR



## Vin Detector

	Min.	typ.	Max.
L-->H	17.430V	17.901V	18.384V
H-->L	16.976V	17.262V	17.728V



## ACIN

Precharge detector	Min.	typ.	Max.
L-->H	14.991V	15.381V	15.782V
H-->L	13.860V	14.247V	14.621V

## BATT ONLY

Precharge detector	Min.	typ.	Max.
L-->H	7.196V	7.349V	7.505V
H-->L	6.138V	6.214V	6.056V

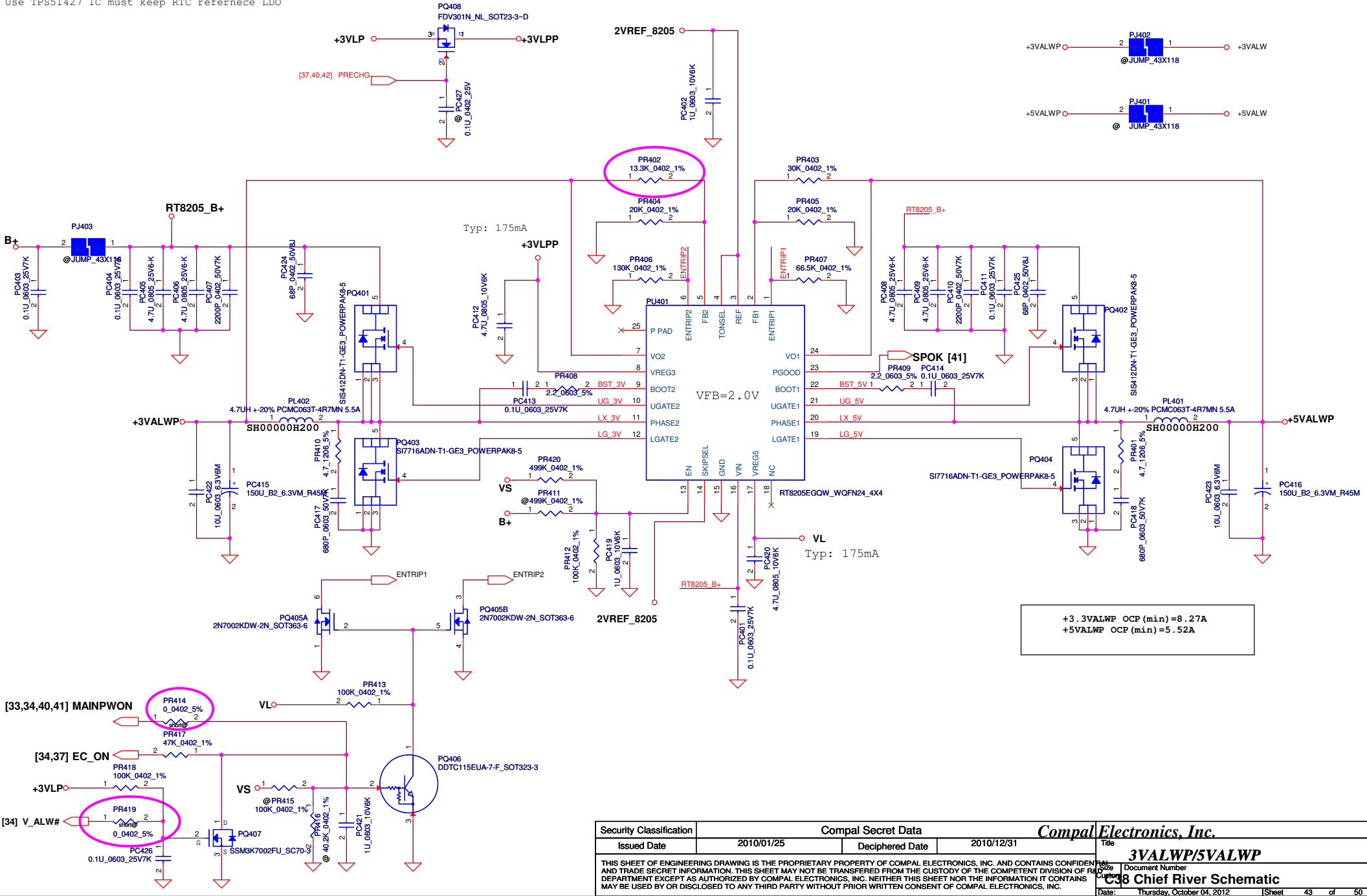
Security Classification	Compal Secret Data			Title	
Issued Date	2010/01/25	Deciphered Date	2010/12/31	PWR DCIN / Vin Detector / Pre-charge	
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Note:  
 Use TPS51125 IC can remove RTC refernece LDO  
 Use TPS51427 IC must keep RTC refernece LDO



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[34,39] SYSON

2011\_0801 JP504 form  
43x118 change to 43x79

+5VALW

[34,39,42,46,9] SUSP#

2011\_0801 JP504 form  
43x118 change to 43x79

+5VALW

[19,34] M\_PWR\_ON

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2010/01/25	Deciphered Date	2010/12/31	Title	PWR-+1.5VP/+1.8VSP
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Date:	Thursday, October 04, 2012	Sheet	44	of	50

2011\_0801 JP505 form  
43x118 change to 43x79

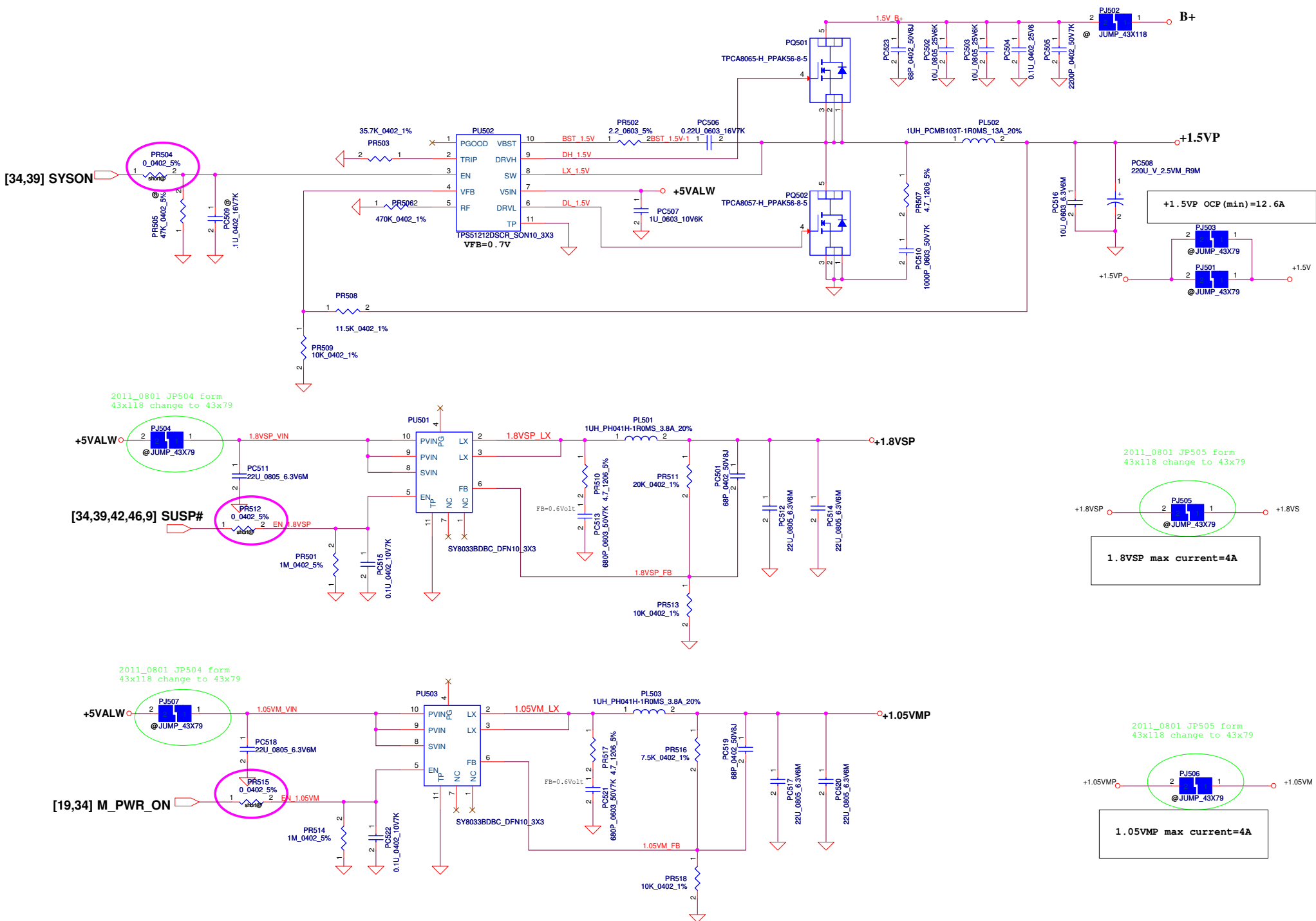
+1.8VSP

1.8VSP max current=4A

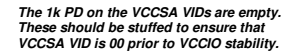
2011\_0801 JP505 form  
43x118 change to 43x79

+1.05VMP

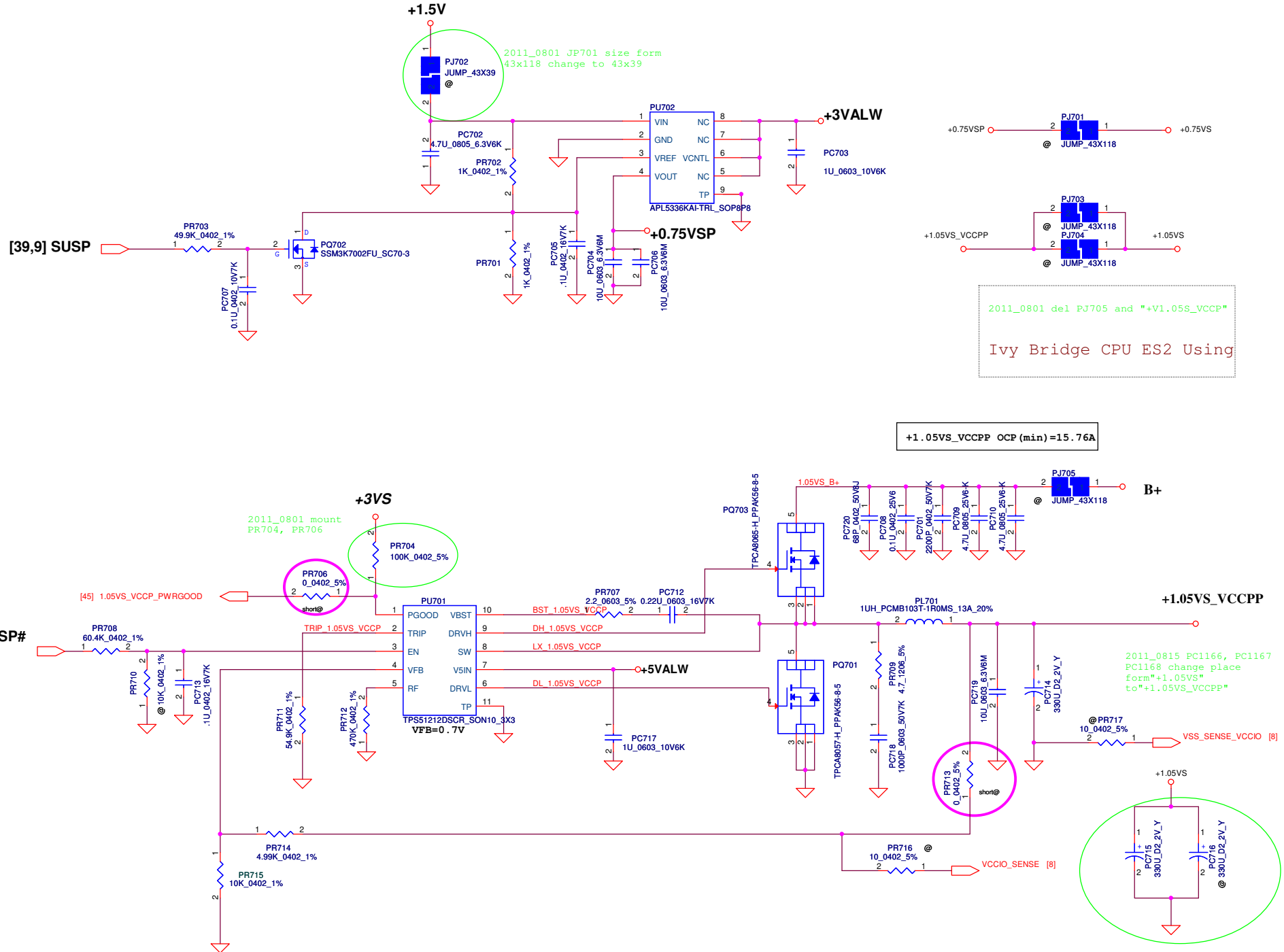
1.05VMP max current=4A



output voltage adjustable network



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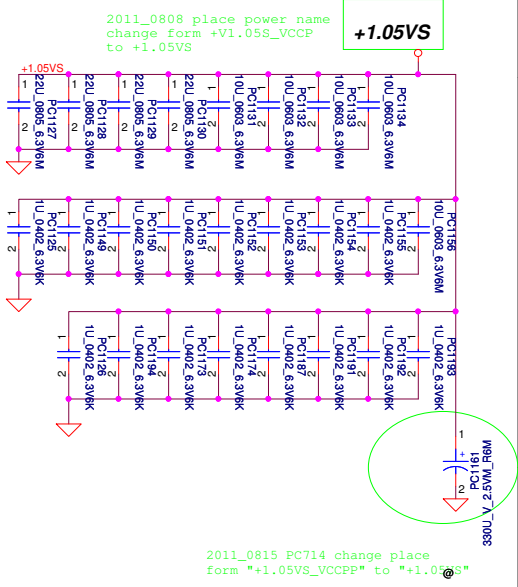
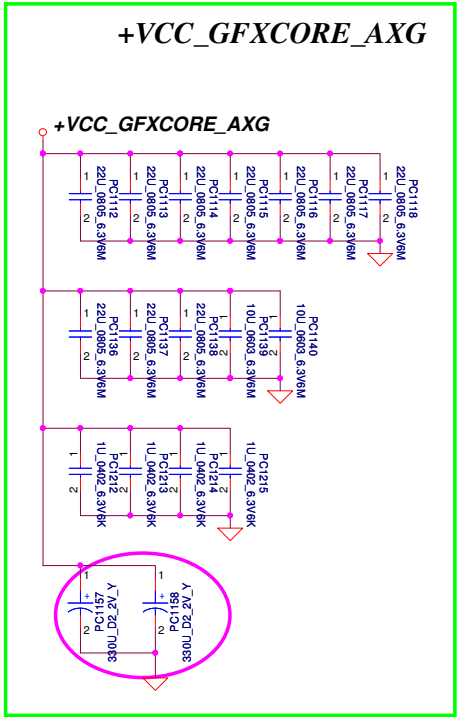
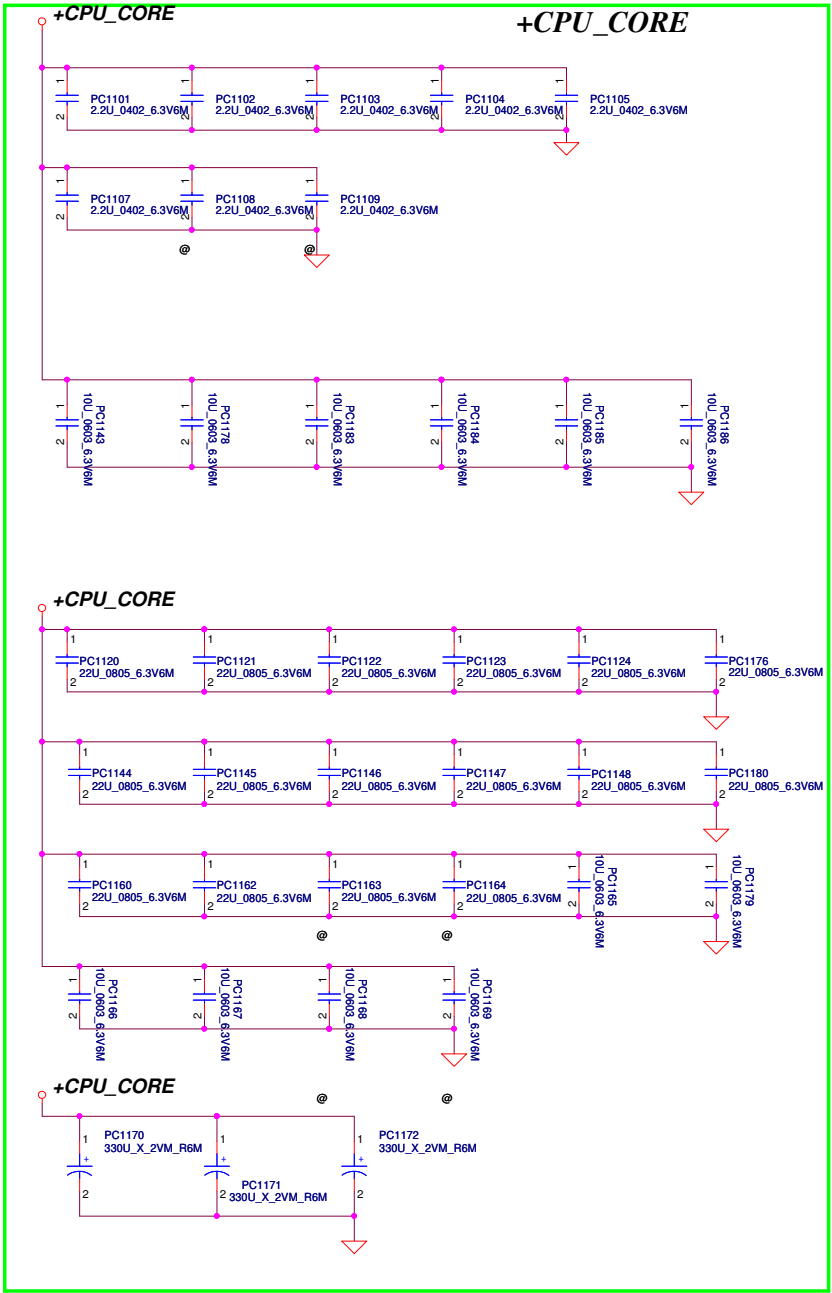


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Issued Date	2008/09/15	Deciphered Date	2009/09/15	PWR - PROCESSOR DECOUPLING	
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## Version change list (P.I.R. List)

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for PWR

Item	Reason for change	PG#	Modify List	Date	Phase
1	NCP6132A update SPEC	P47	Remove PR916, PR945	2012/06/05	
2	Thermal verify PH1 setting point	P41	Change PR208 value to 18.2K	2012/06/08	
3	ME demand : Keyboard assembly issue	P44	Change PL401, PL402 to High=2.4mm (SH000000H200)	2012/06/08	
4	Noise is PASS, Don't need 33u CAP	P48	Remove PC1023, PC1024	2012/06/08	
5					
6					
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Issued Date	2009/01/06	Deciphered Date	2009/01/06	Title	
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				C38 Chief River Schematic	
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